

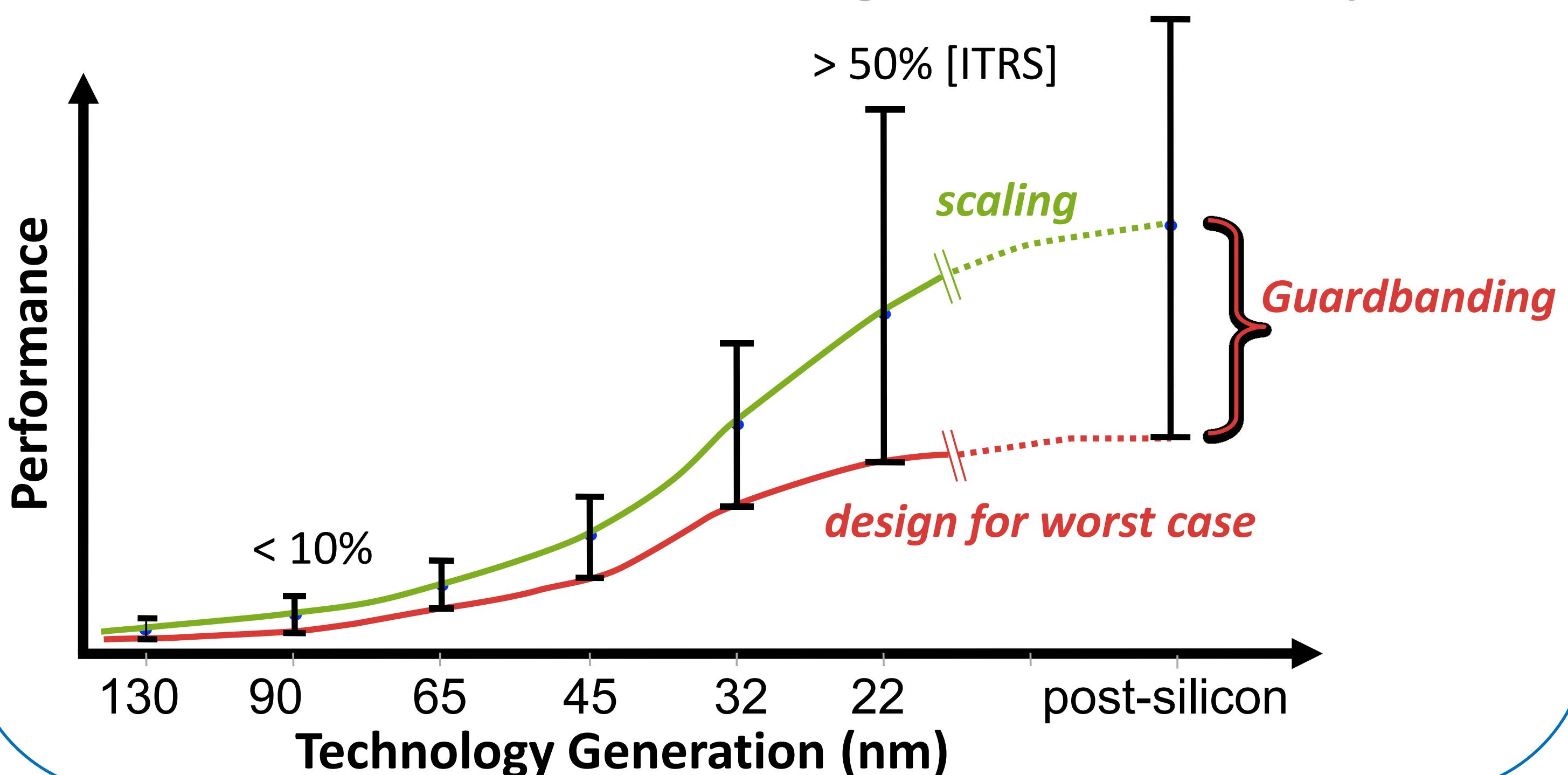
Task Scheduling Strategies to Mitigate Hardware Variability in Embedded Shared Memory Clusters

Abbas Rahimi[†], Daniele Cesarini[‡], Andrea Marongiu^{‡*}, Rajesh K. Gupta[†], Luca Benini^{‡*}

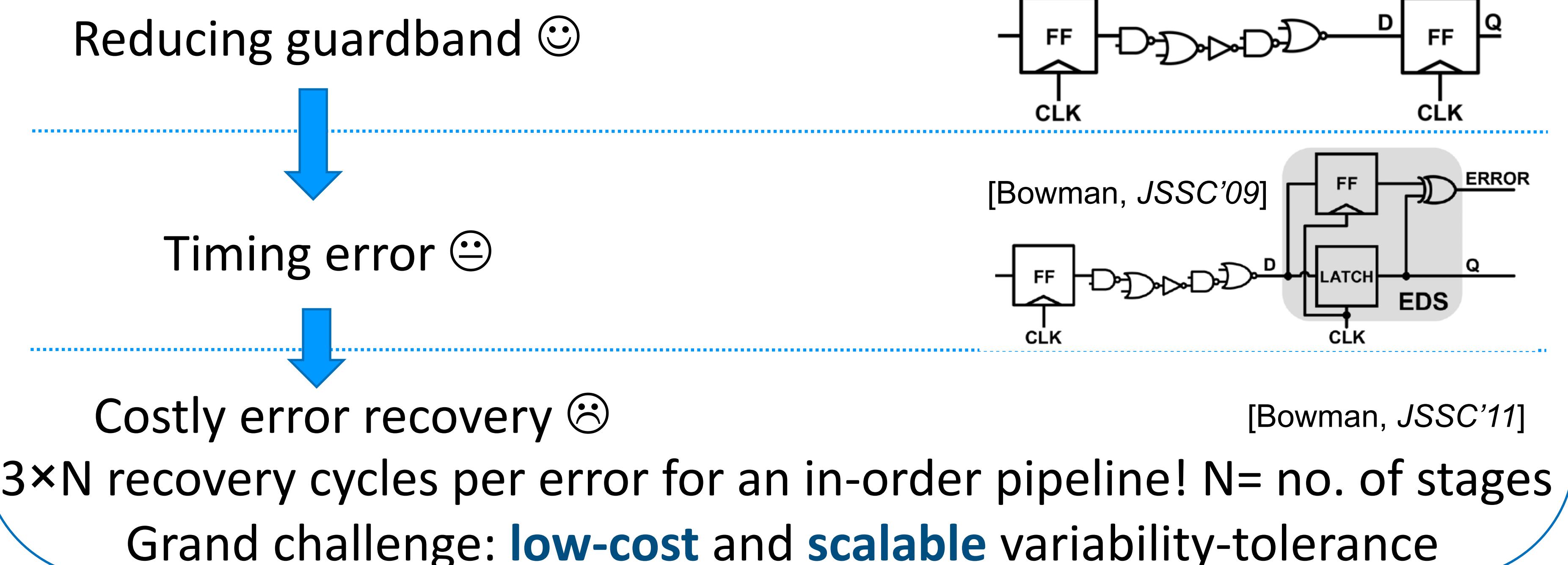
[†]UC San Diego, [‡]University of Bologna, ^{*}ETH Zurich



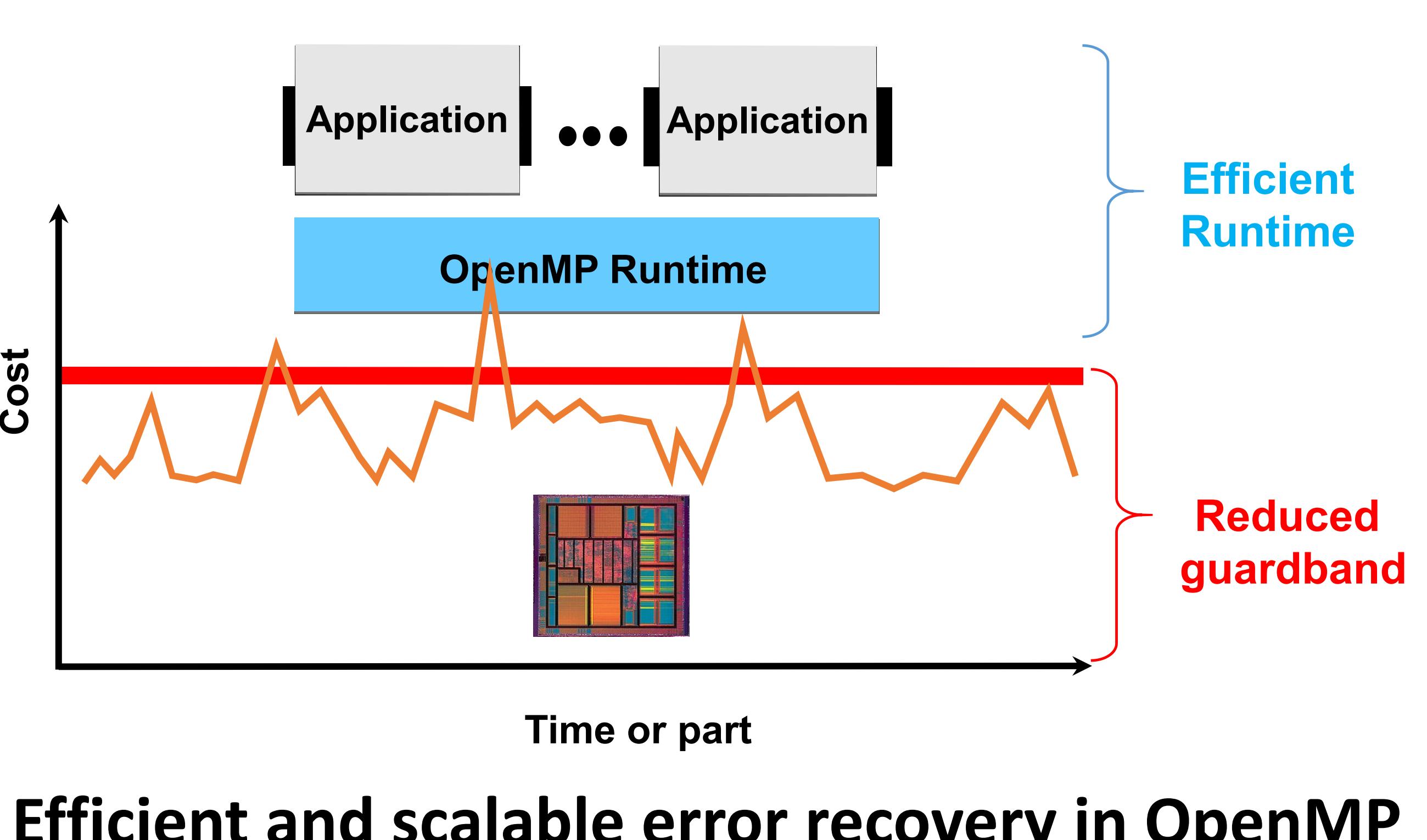
Ever-Increasing Variability



Reduced Guardband Causes Timing Errors

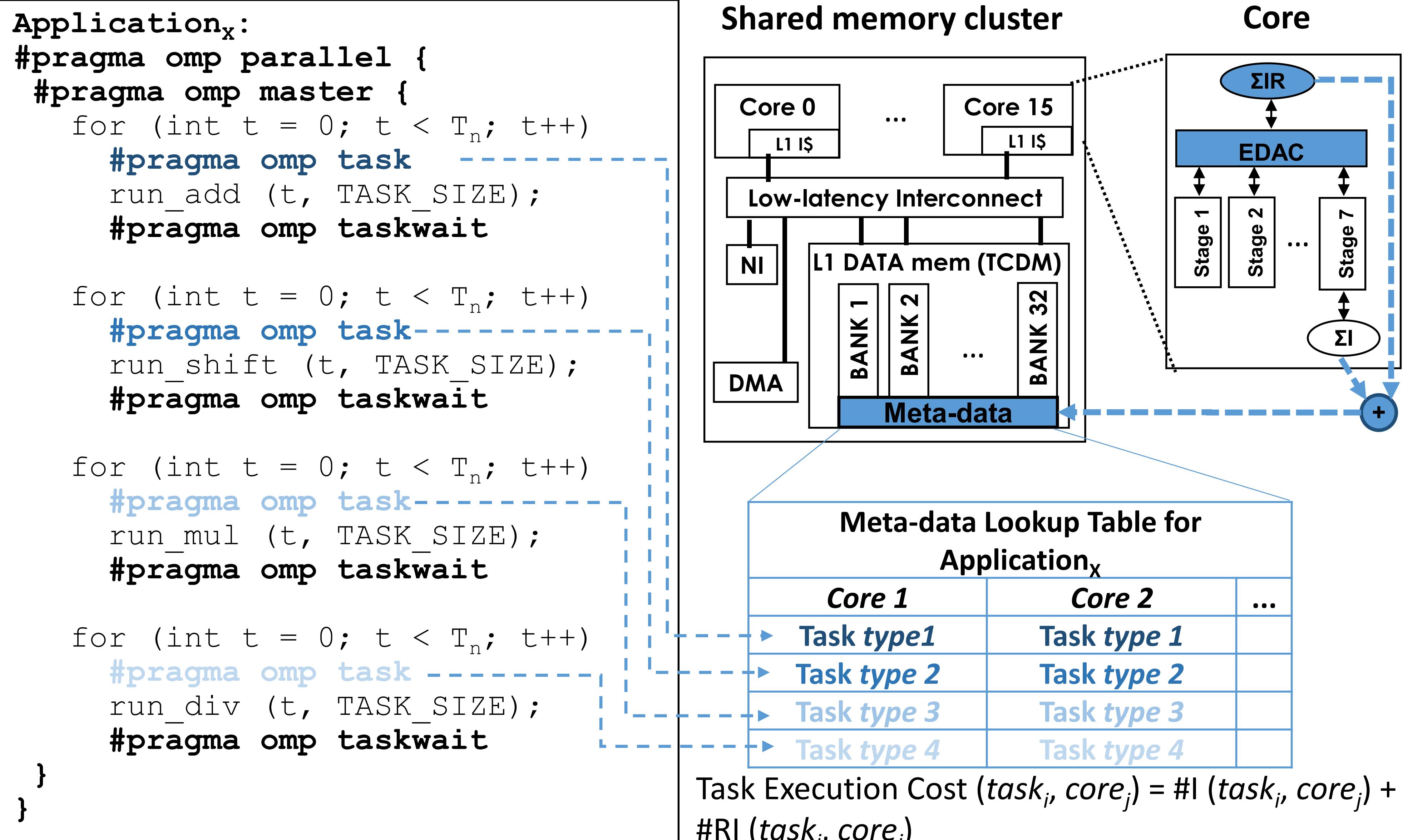


Key Idea



Efficient and scalable error recovery in OpenMP runtime

Online Meta-data Characterization



Time and Energy Saving

