

Three-dimensional Quantum Simulation of Silicon Nanowires

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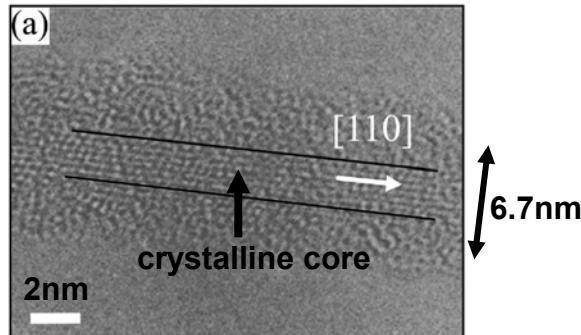
¹ ETH Zürich

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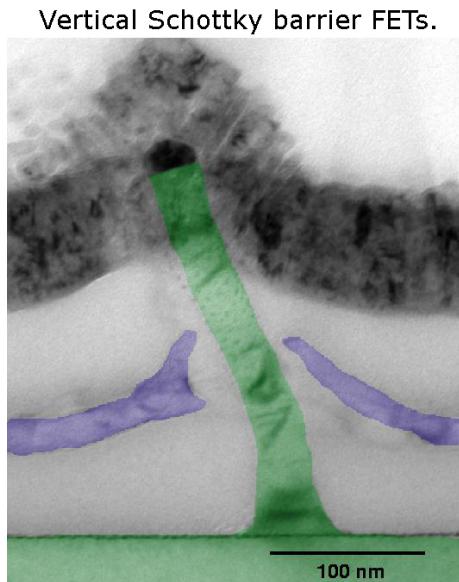


Outline

- **Introduction**
- **Simulation method**
 - Band structure, transport, electrostatics
- **Quantum-ballistic currents**
 - On-current for different channel orientations
 - Source-drain tunneling
 - Interface roughness
- **Gate tunneling leakage**
- **Limit of large cross sections**
- **Incoherent scattering**
- **Conclusion**



Cui et al., APL 78(15), 2214 (2001)



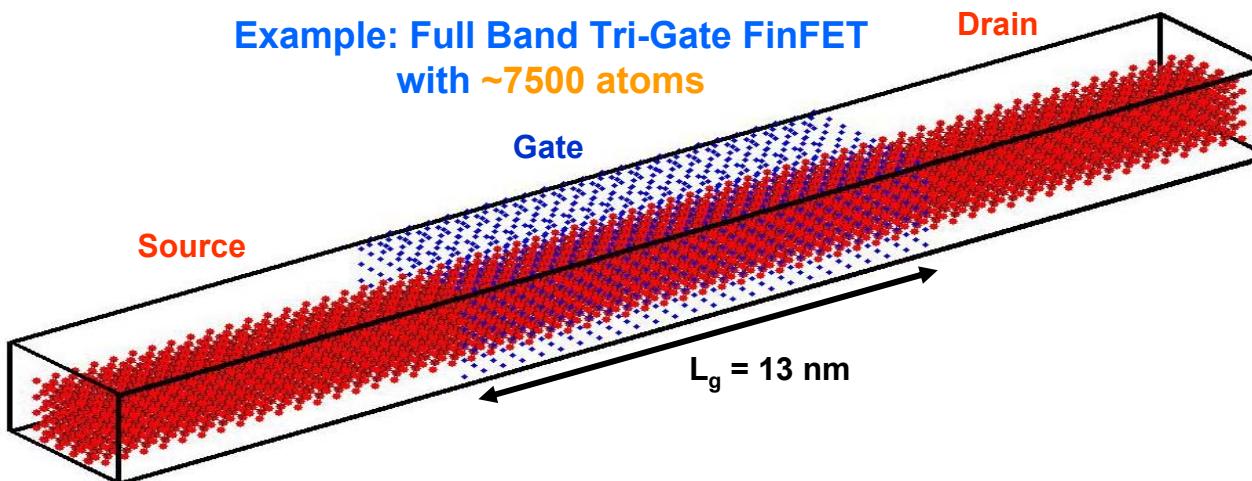
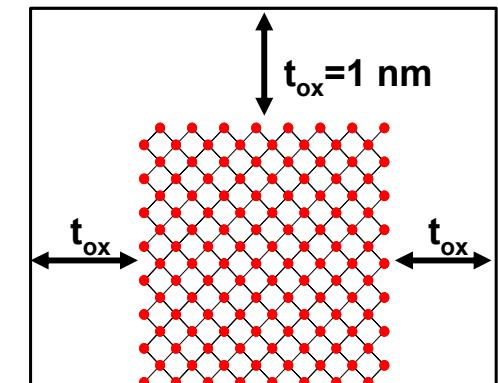
IBM Zürich, Walter Riess

- Si NWs are ‘post-CMOS’ candidates, both for transistors and wire connectors
- grown NWs with different crystal orientations and cross sections, working transistors demonstrated
- if cross section $< 5 \times 5 \text{ nm}^2$, strong confinement => band structure effects become important
- predictive simulation requires: accurate band structure model, quantum transport solver (OBCs), self-consistent electrostatics
- main challenges: CPU time, incoherent scattering

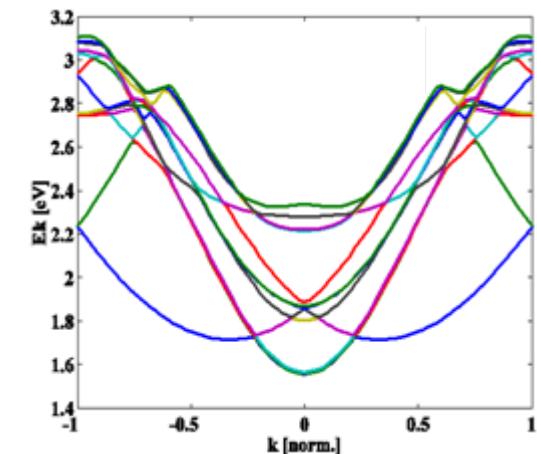
Band structure

- **$sp^3d^5s^*$ tight-binding method**
- bulk band structure exactly reproduced
- inclusion of strain, defects, surface roughness possible
- extension to nanostructures straight-forward
- gate tunneling and b2b tunneling possible
- high computational effort required for nanostructures, since 10 bands involved without spin, 20 bands with spin
- **bulk TB parameters, no lattice relaxation**

Atomistic description of a [100] nanowire with 2.1×2.1 nm² cross section

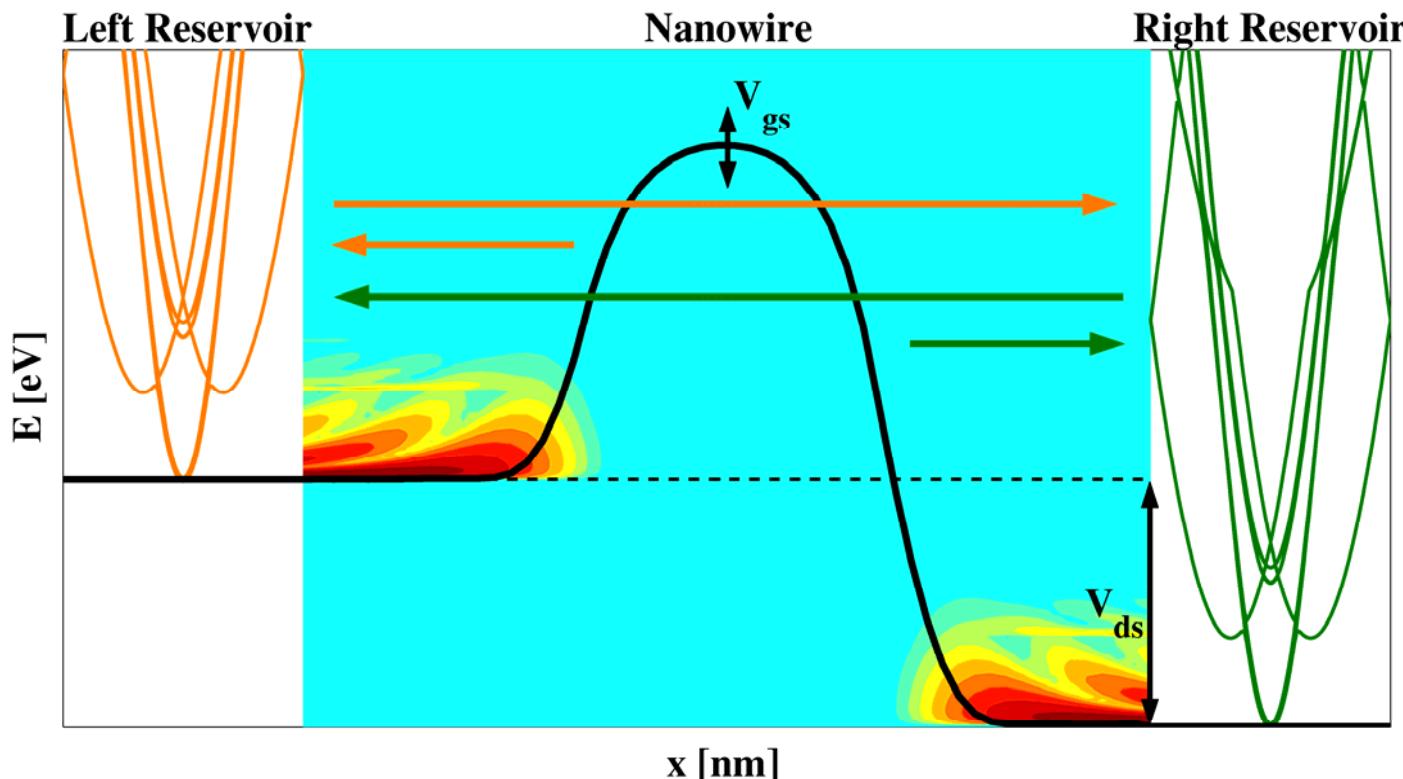


TB band structure



Transport

Cut along the transport direction x in the nanowire



Wave functions are injected from the reservoirs and either **reflected** or **transmitted** to the other side. Band structure of reservoirs can be calculated because semi-infinite. At each energy all the k -states with **positive** (left) or **negative** (right) **velocity** are selected for injection.

Transport

WF formalism

Schrödinger Equation

$$H|\psi_E\rangle = E|\psi_E\rangle$$

Tight-Binding ansatz for the wave function

$$\langle \mathbf{r} | \psi_E \rangle = \sum_{\sigma,ijk} C_{ijk}^{\sigma}(E) \phi_{\sigma}(\mathbf{r} - \mathbf{R}_{ijk})$$

Löwdin orbitals

Scattering Boundary Conditions => ordinary eigenvalue problem!!

$$M(E, A)\phi_{k(E)}(A) = \lambda(k(E))\phi_{k(E)}(A)$$

Final form of the problem in the Wave Function formalism

$$\mathbf{H}_{tot} \cdot \mathbf{C}_{p,n}^{\sigma}(k) = \mathbf{I}_{0,p,n}(k)$$

Orbital-coefficient vector

Transport

Carrier and current density

$$n(x, \mathbf{r}_s) = \frac{1}{N_x} \sum_{n,p,\sigma} \sum_i \sum_{\mathbf{R}_s} |C_{i,p,n}^{\sigma}(\mathbf{R}_s, k)|^2 f(E_{p,n}(k) - \mu_p) \delta(x - x_i) \delta(\mathbf{r}_s - \mathbf{R}_s)$$

$$\begin{aligned} \mathbf{J}(\mathbf{r}) = & \sum_{i_1, i_2} i \frac{e}{2\hbar} \sum_{p, n_p} \frac{\Delta}{2\pi} \int dE \left(H_{i_1 i_2} C_{i_2, p, n_p} C_{i_1, p, n_p}^* - C_{i_1, p, n_p} C_{i_2, p, n_p}^* H_{i_2 i_1} \right) \times \\ & \times \left| \frac{dE}{dk_{p, n_p}} \right|^{-1} f(E - \mu_p) (\mathbf{R}_{i_2} - \mathbf{R}_{i_1}) \delta(\mathbf{r} - \mathbf{R}_{i_1}) \end{aligned}$$

Alternatively, in Landauer-Büttiker formula with transmission T(E)

$$T(E) = \sum_{n,m} |C_{N_s+1, p=1, n}(k_m)|^2 \left| \frac{dE}{dk_m} \right| \left| \frac{dE}{dk_n} \right|^{-1}$$

NEGF formalism

$$n(\mathbf{r}) = -i \sum_j \int \frac{dE}{2\pi} G_{jj}^<(E) \delta(\mathbf{r} - \mathbf{R}_j)$$

$$\mathbf{J}(\mathbf{r}) = \sum_{i_1} \sum_{i_2} \frac{e}{2} \left(H_{i_1 i_2} G_{i_2 i_1}^< - G_{i_1 i_2}^< H_{i_2 i_1} \right) (\mathbf{r}_{i_2} - \mathbf{r}_{i_1}) \delta(\mathbf{r} - \mathbf{r}_{i_1})$$

Transport

WF \leftrightarrow NEGF (if no incoherent scattering)

$$G_{ij}^<(E) = \sum_p \sum_n \underbrace{C_{i,p}(k_n) C_{j,p}^T(k_n)}_{(\mathbf{t}_b \cdot \mathbf{N}_A) \times (\mathbf{t}_b \cdot \mathbf{N}_A)} \left| \frac{dE(k_n)}{dk_n} \right|^{-1} f(E(k_n) - \mu_p)$$

When NEGF? In case of incoherent scattering.

When WF? Otherwise, because CPU time is greatly reduced!

Iterative solutions ($N = \mathbf{t}_b \cdot \mathbf{N}_A$)

$$(\mathbf{E} - \mathbf{H} - \mathbf{t}_{10} \cdot \mathbf{g}_{00}^R \cdot \mathbf{t}_{01}) \cdot \mathbf{g}_{00}^R = \mathbf{I}$$

Generalized eigenvalue problem ($N = 2 \mathbf{t}_b \cdot \mathbf{N}_A$)

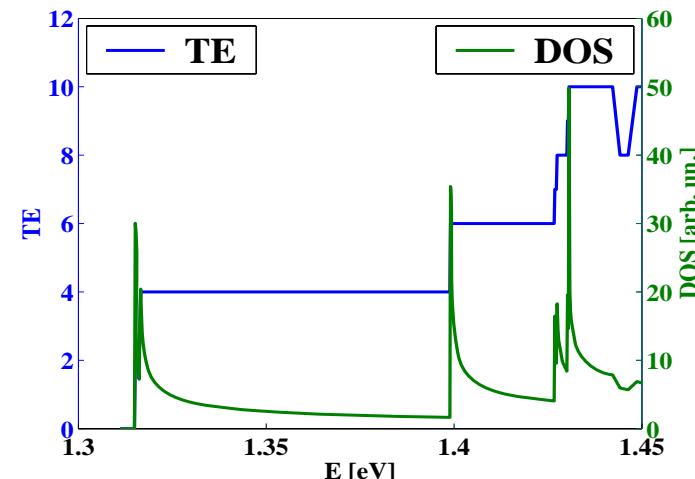
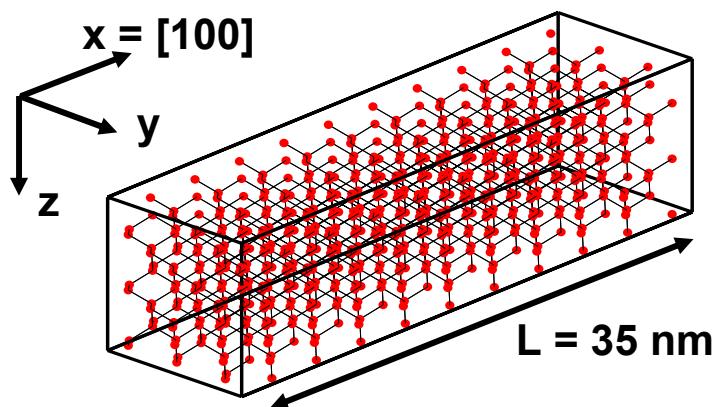
$$\mathbf{A}(\mathbf{E}) \cdot \mathbf{C}_n = \exp(i\mathbf{k}_n(\mathbf{E})\Delta) \cdot \mathbf{B}(\mathbf{E}) \cdot \mathbf{C}_n$$

Shift-and-invert + ordinary eigenvalue problem ($N < \mathbf{t}_b \cdot \mathbf{N}_A$) (PRB 74, 205323 (2006))

$$\mathbf{M}(\mathbf{E}) \cdot \mathbf{C}_n = \lambda_n(\mathbf{k}_n(\mathbf{E})) \cdot \mathbf{C}_n$$

Transport

Benchmark example: 35 nm long [100] nanowire, 1 energy point



First task: Open Boundary Conditions

$L_y \times L_z \text{ nm}^2$	$t_b \times N_A$	Iterative Solver	Generalized EVP	Ordinary EVP
2.5×2.5	1810	197	506	7.2
2.9×2.9	2420	462	1490	18.5
3.3×3.3	3130	1070	3930	39

All CPU times (in sec) obtained on a Sun Fire with $8 \times 2.8 \text{ GHz}$ AMD processors

Transport

Benchmark example: 35 nm long [100] nanowire, 1 energy point

Second task: Transport problem

#CPU	Umfpack	Pardiso	SuperLU _{dist}	MUMPS	Basis Compression	Recursive GF
1	406	271	560	240	105	1418
2	-	141	258	129	54	-
4	-	84	130	76	31	-
8	-	63	112	56	21	-



The diagram consists of a horizontal double-headed arrow spanning the width of the table. It is divided into two segments by a vertical tick mark at the center. The left segment is labeled "Wave Function" in blue text, and the right segment is labeled "NEGF" in blue text.

All times in **sec** for a $3.3 \times 3.3 \times 35$ nm³ NW without SO coupling

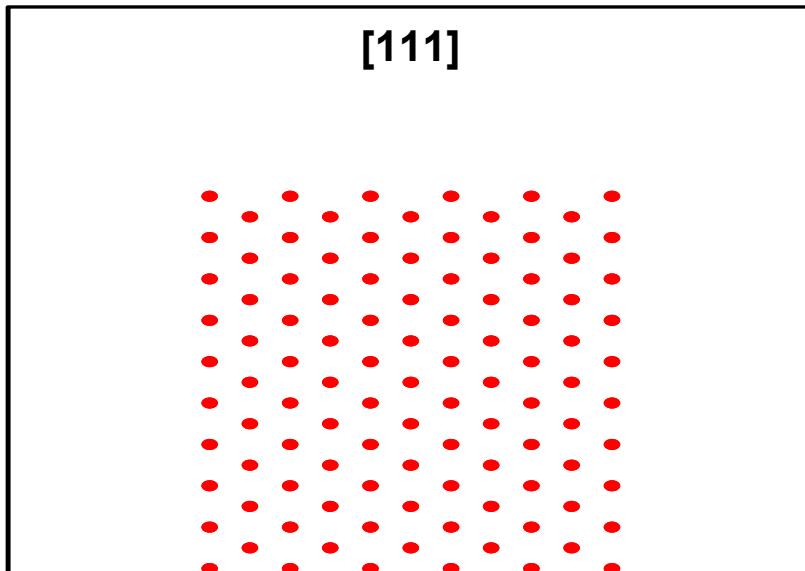
Electrostatics

Grid Generation for Poisson equation

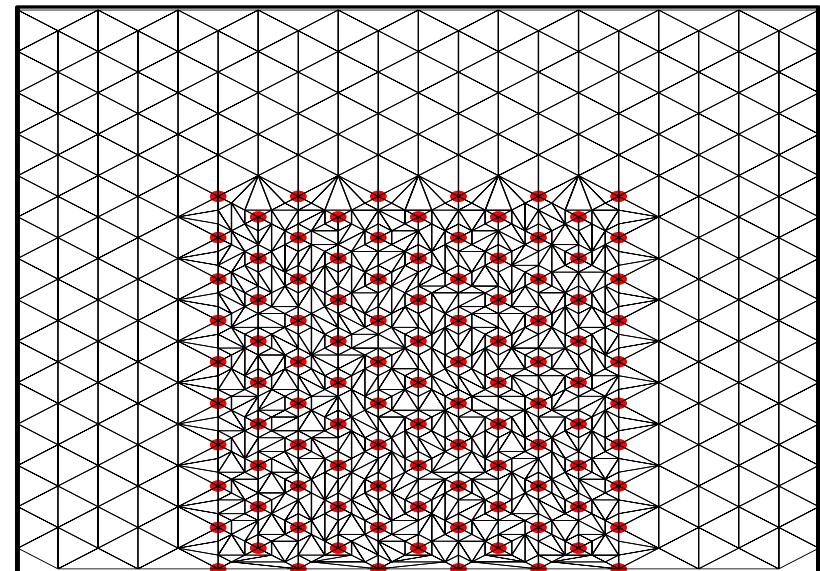
Grid must be general => Delaunay mesh: no data point (atoms) is contained in any triangle's circumcircle (2D) or in any tetrahedron's circumspheres (3D).

Carriers localized around atom positions

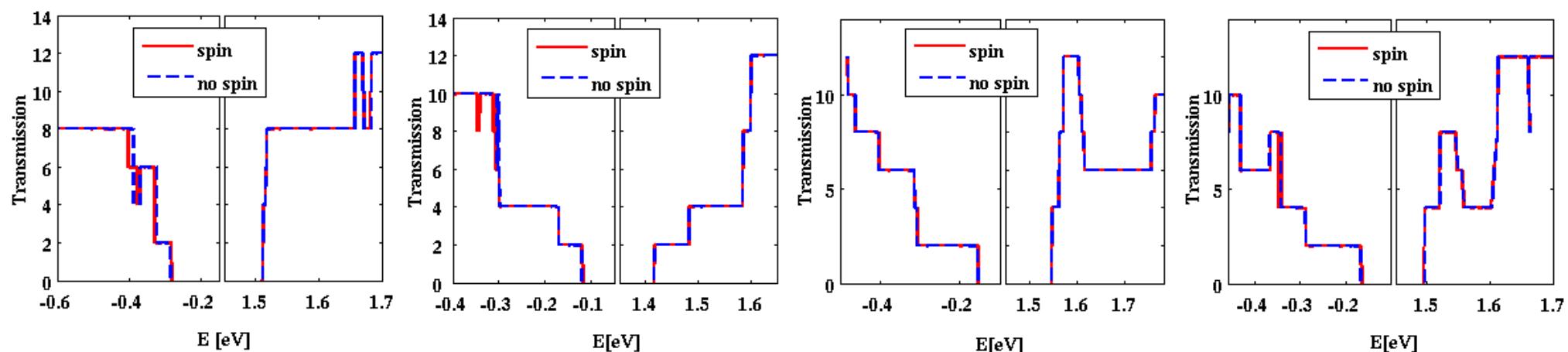
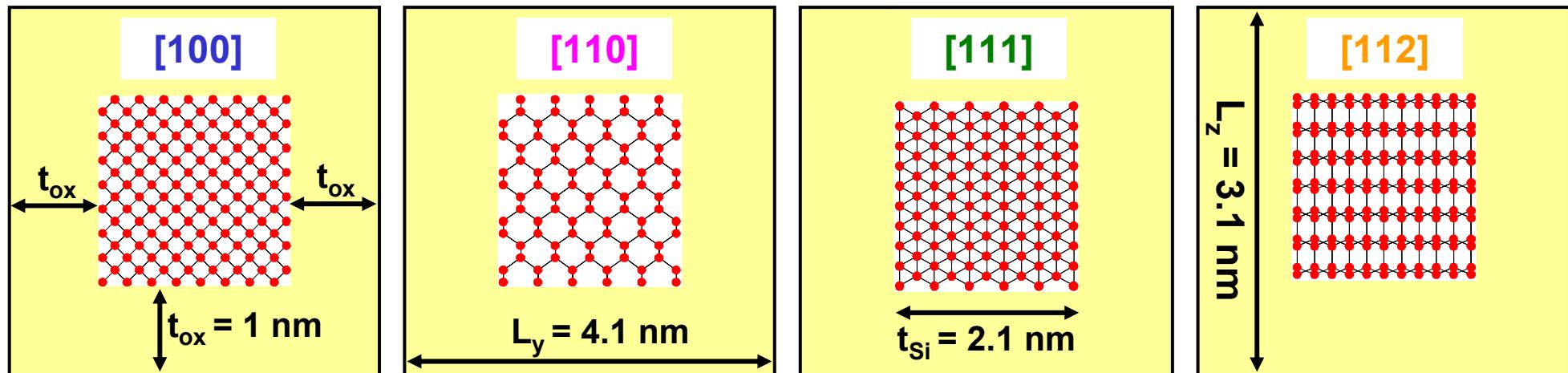
$$n(\mathbf{r}) = \sum_i n_i \delta(\mathbf{r} - \mathbf{r}_i)$$



Projection of FEM mesh on cross section.
No charge in the oxide => larger elements

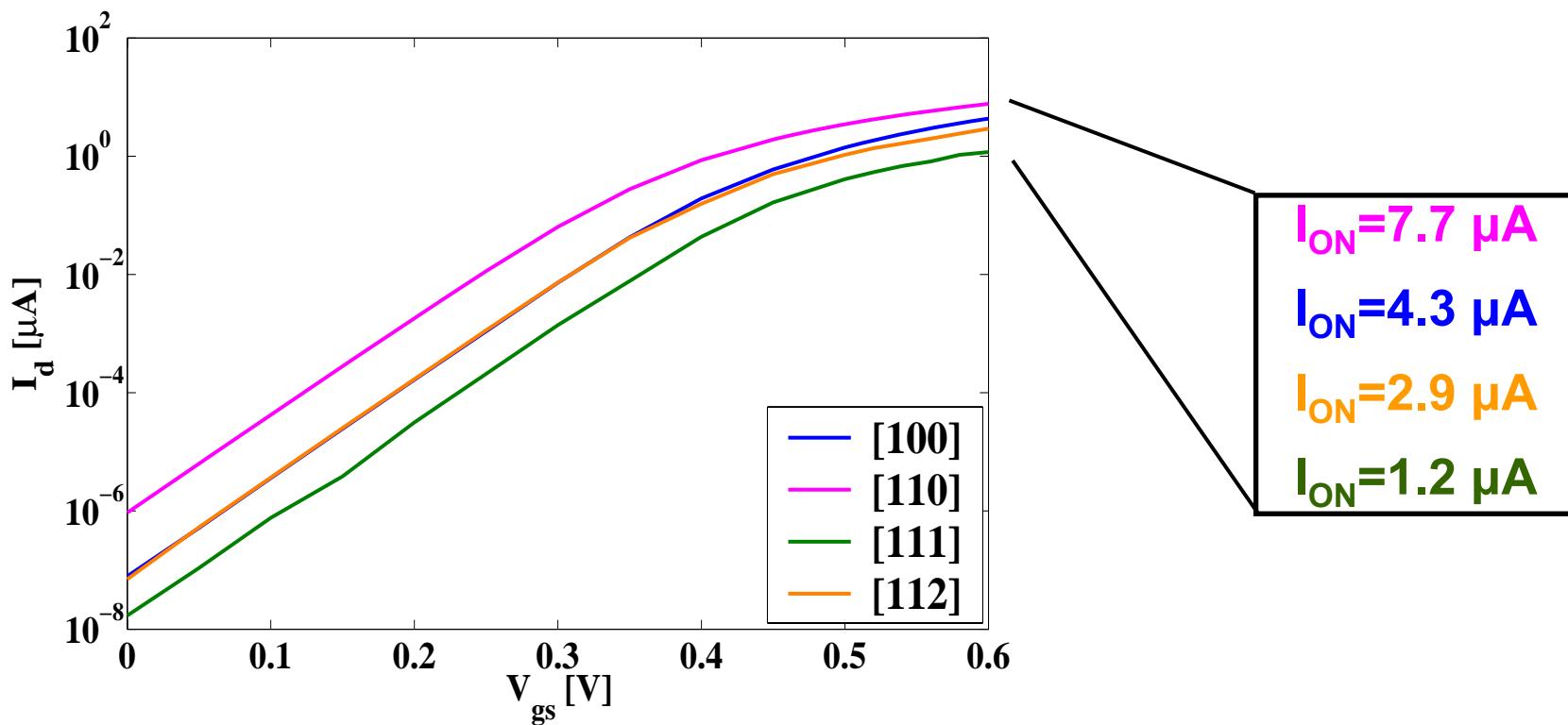


On-current for different channel orientations



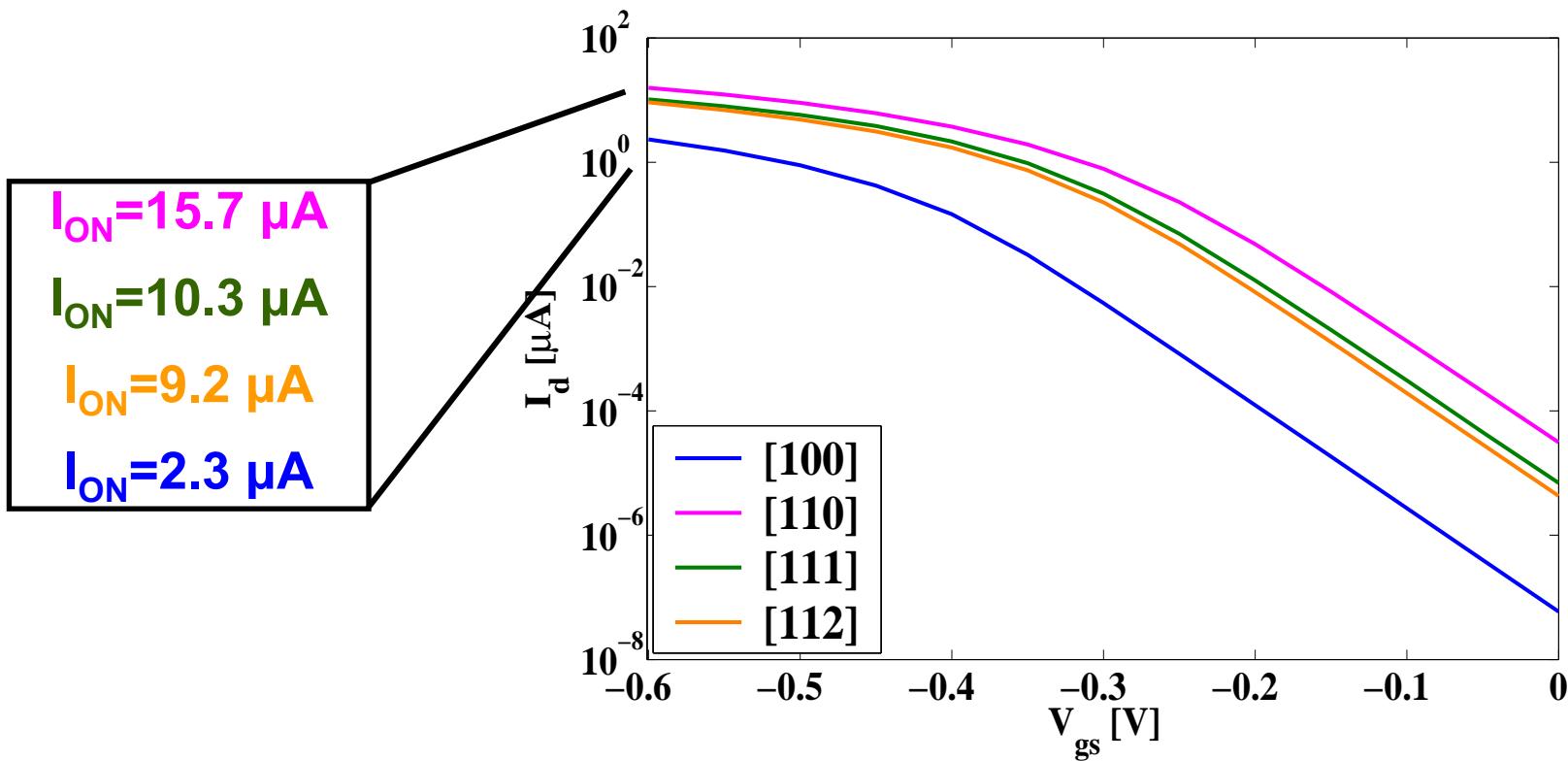
On-current for different channel orientations

Full-band (FB) transfer characteristics: I_d - V_{gs} at $V_{ds}=0.4$ V
n-FET with [100], [110], [111], and [112], $L_g=13$ nm



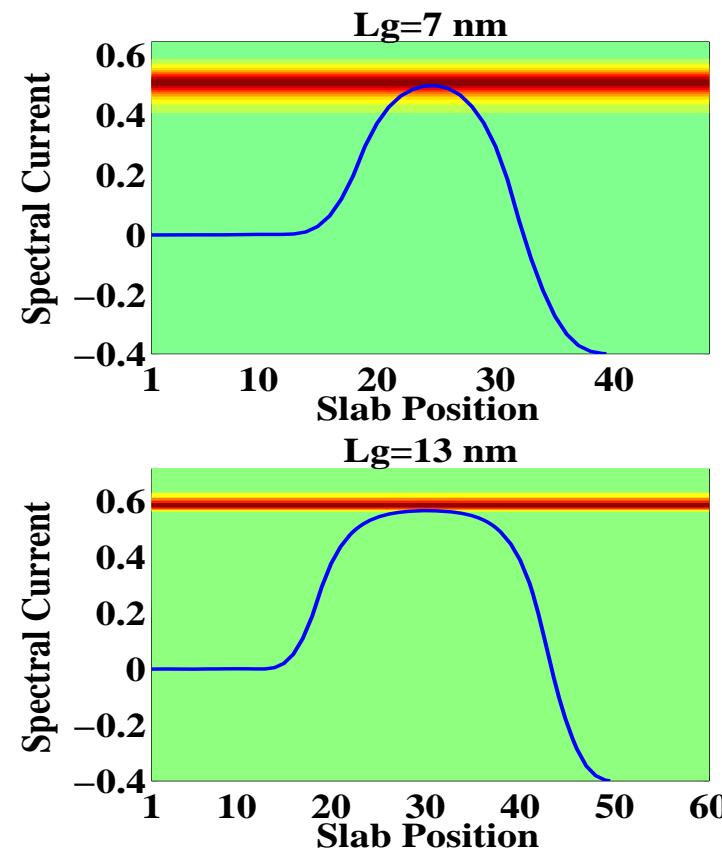
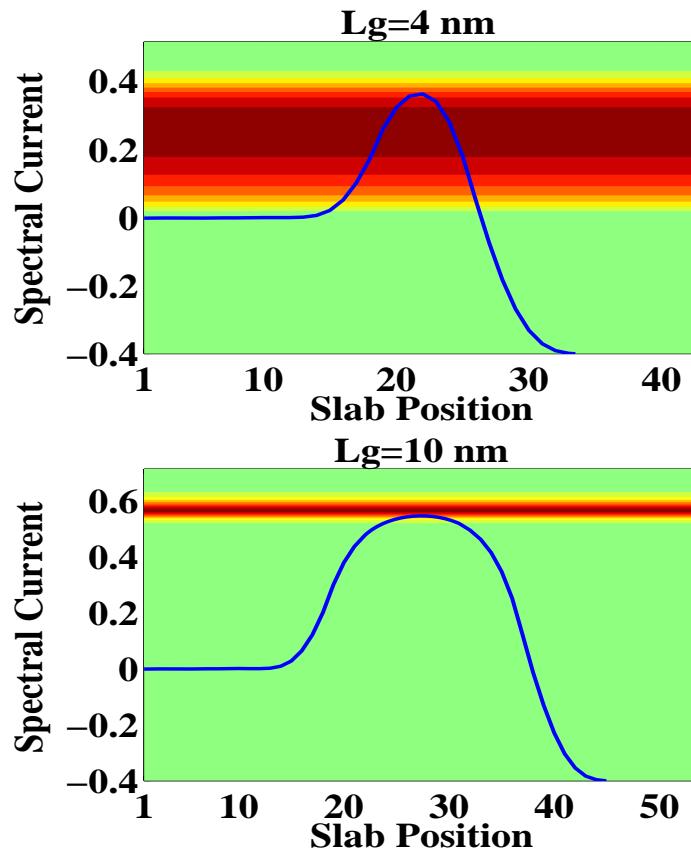
On-current for different channel orientations

Full-band (FB) transfer characteristics: I_d - V_{gs} at $V_{ds} = -0.4$ V
p-FET with [100], [110], [111], and [112], $L_g = 13$ nm



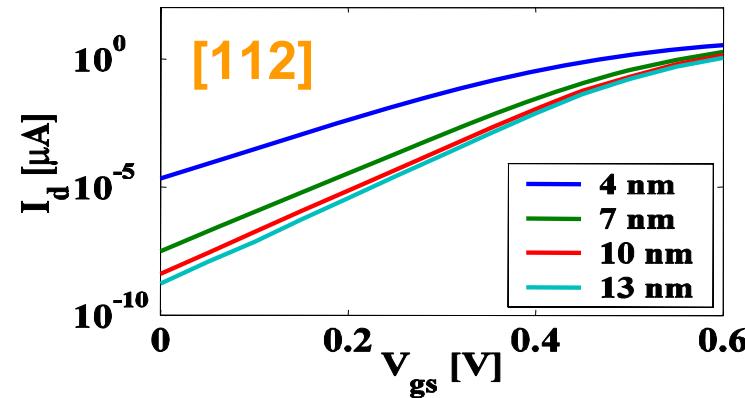
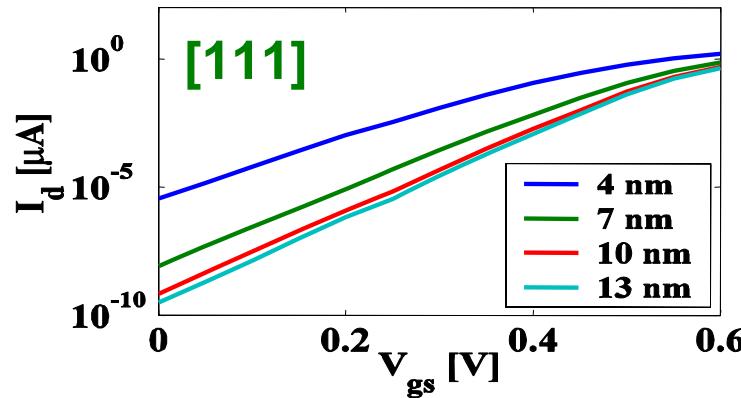
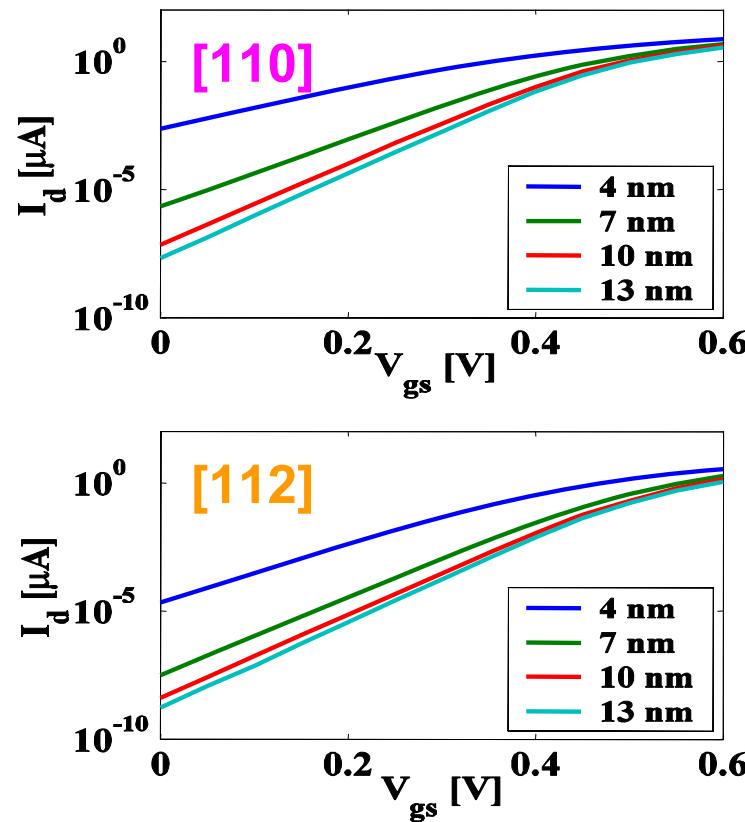
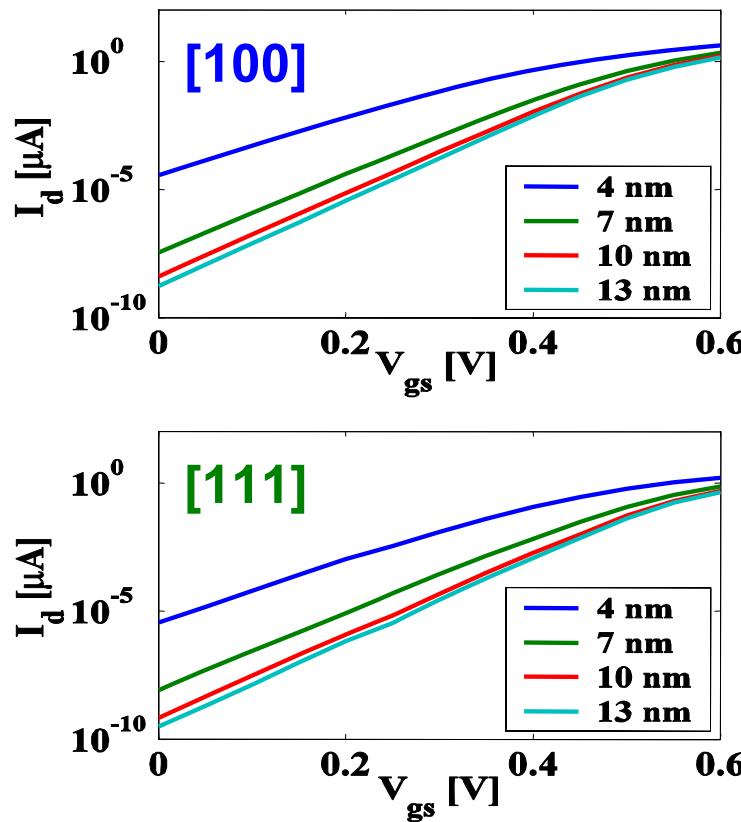
Source-drain tunneling

L_g influence for nanowire n-FET with [100] channel



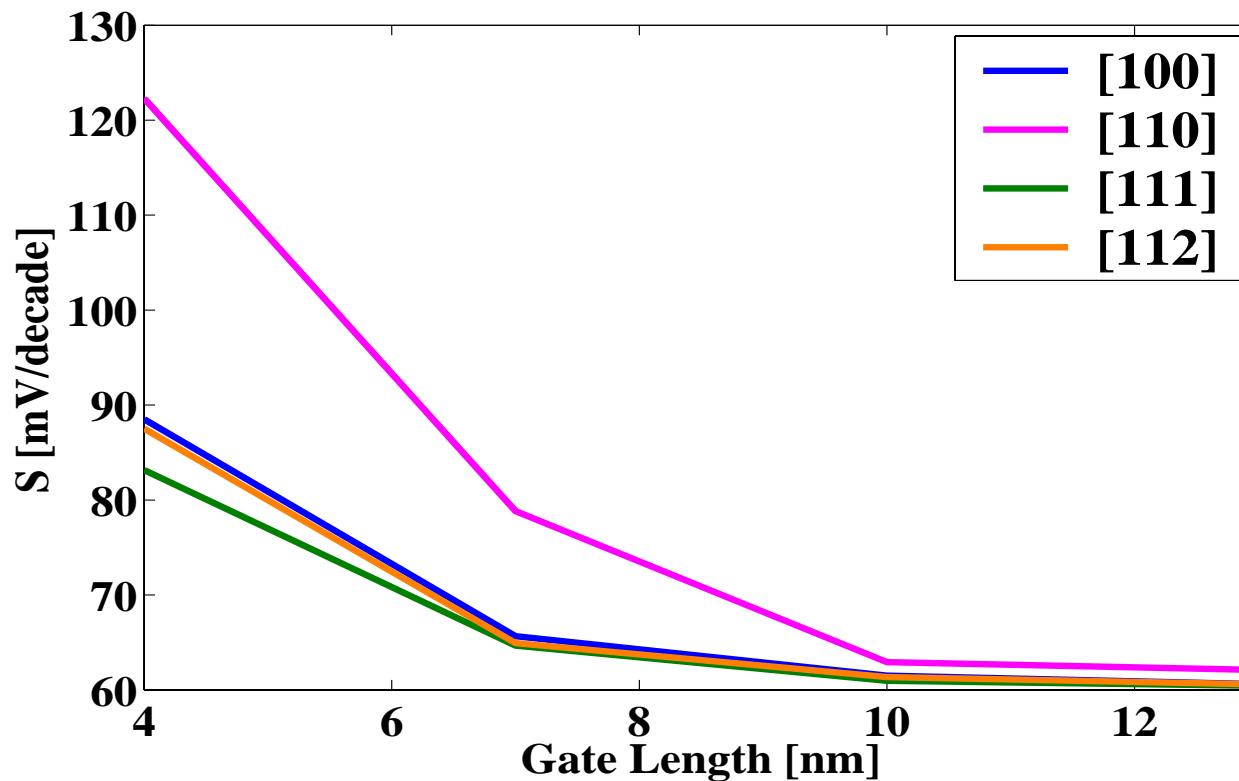
Source-drain tunneling

I_d - V_{gs} @ $V_{ds} = 0.4$ V for 4 different channel lengths (4 nm, 7 nm, 10 nm, and 13 nm) and for [100], [110], [111], [112]



Source-drain tunneling

Sub-threshold swing S as function of gate length L_g
Low effective mass => high on-current / strong tunneling

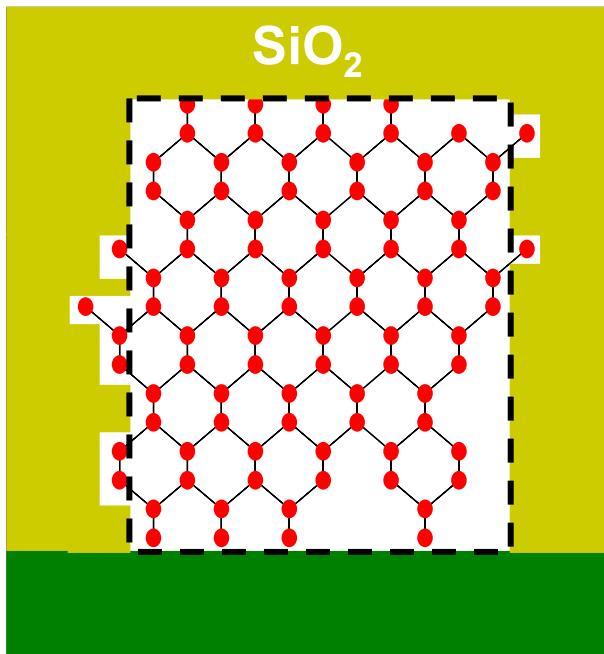


Interface roughness

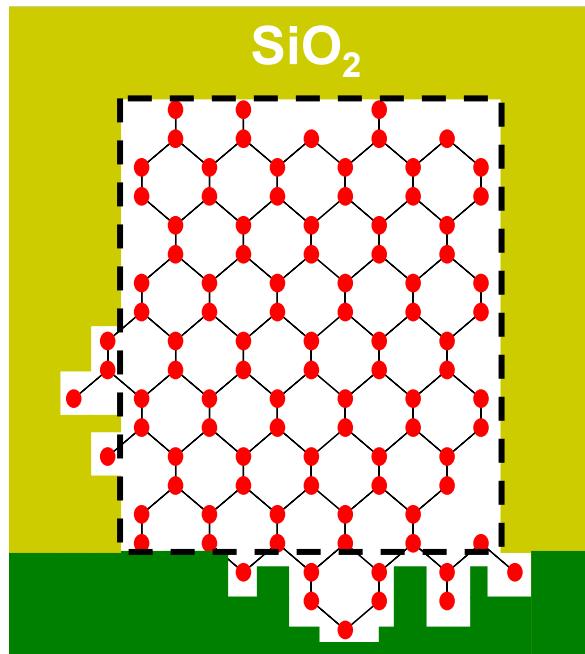
Process variations => cross section variations

Example: [110] nanowire

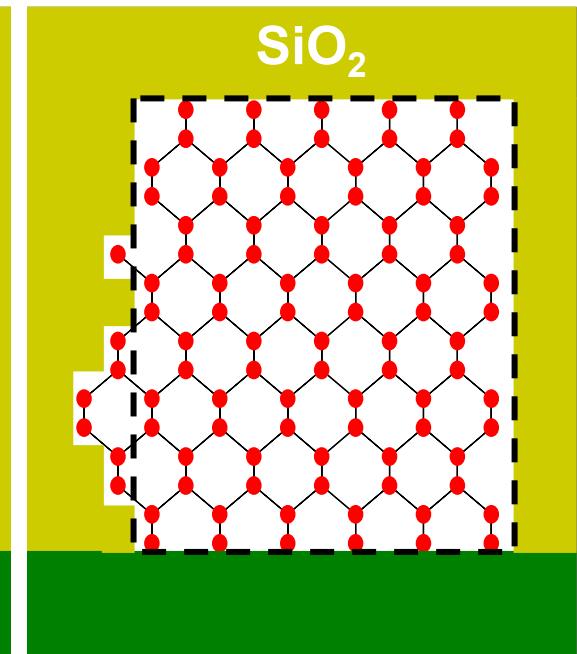
$x=14 \text{ nm}$



$x=16 \text{ nm}$



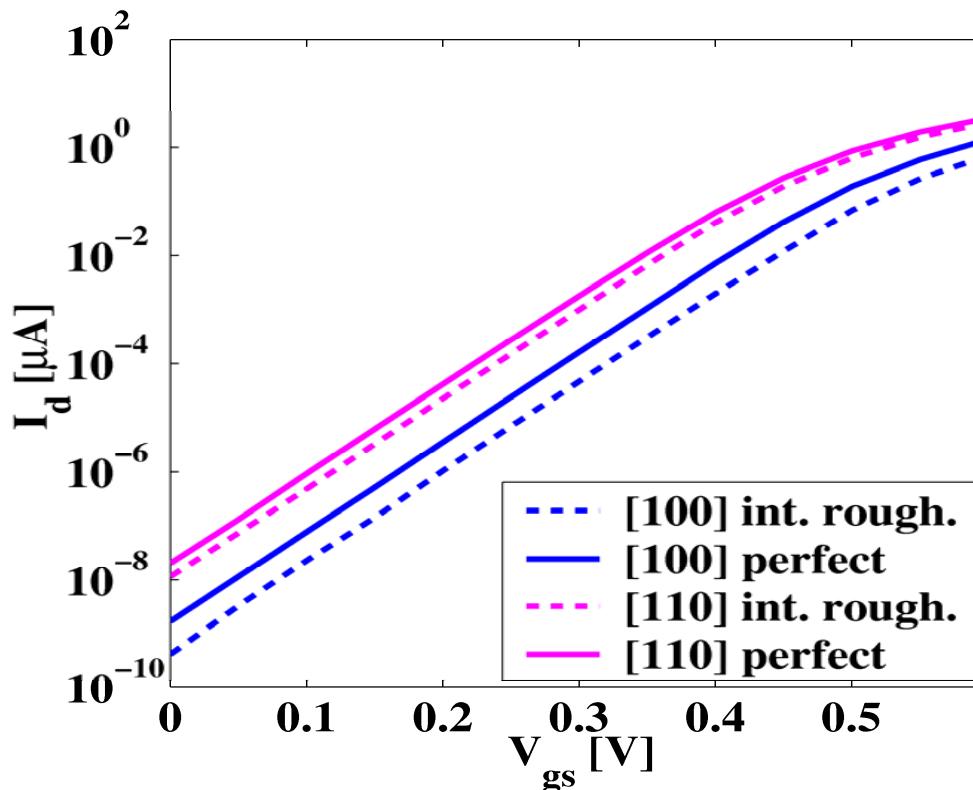
$x=26 \text{ nm}$



$$\text{interface roughness scattering: } S(x) = \Delta^2 \exp(-|x|/L_m)$$

Interface roughness

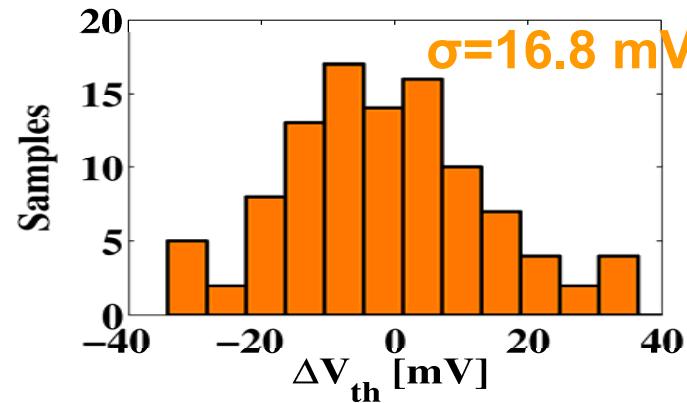
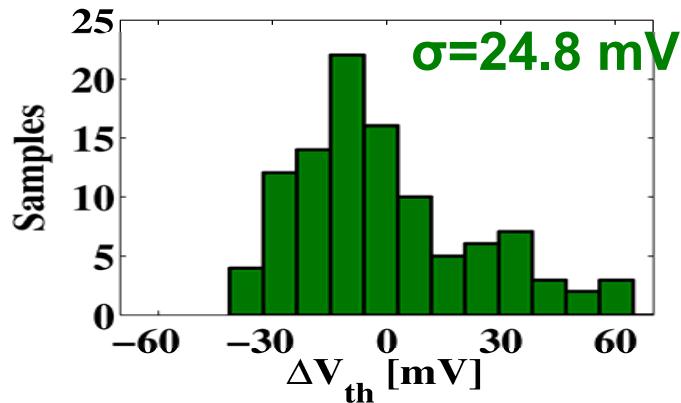
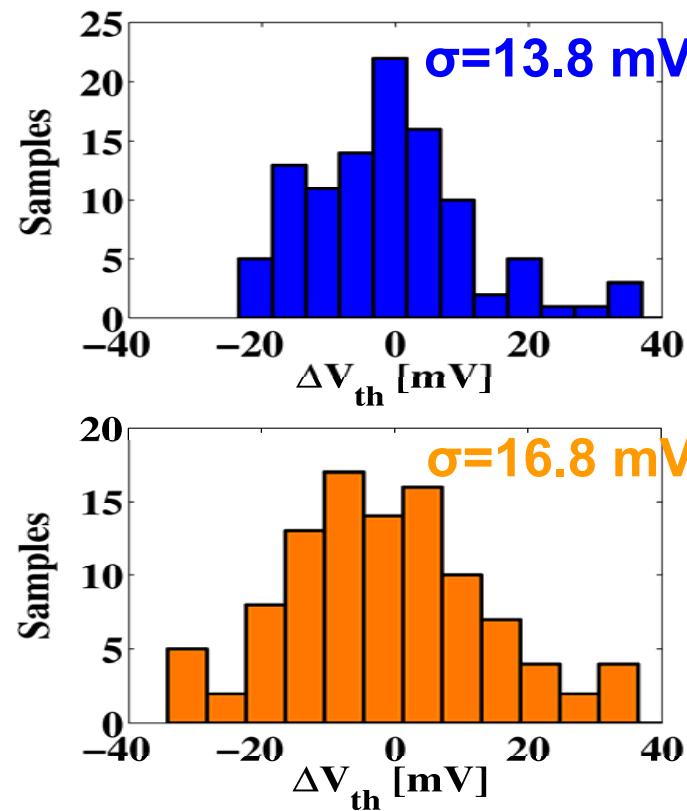
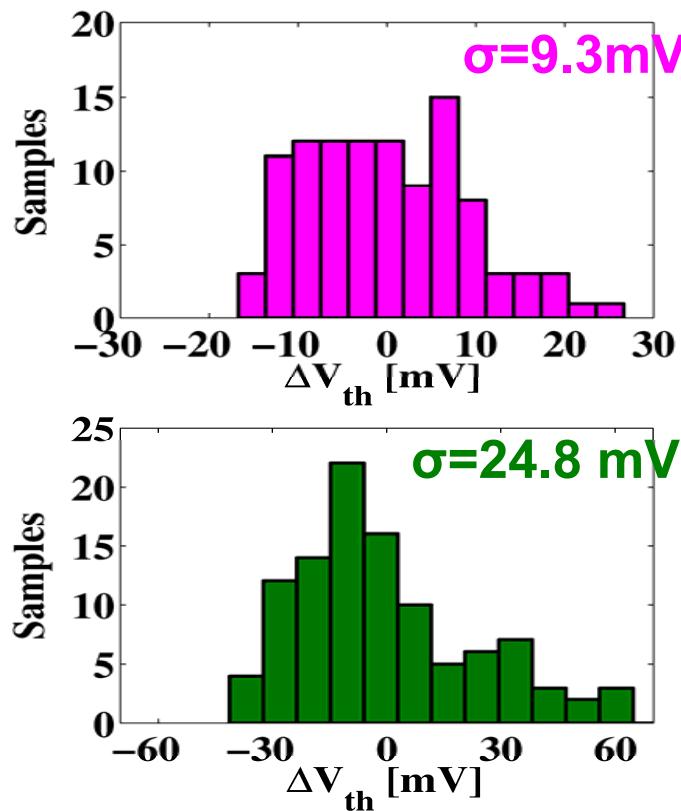
FB I_d - V_{gs} at $V_{ds}=0.4$ V for **one possible** interface realization



- 1) Sub-threshold swing remains constant: $S \approx 60$ mV/dec.
- 2) Threshold voltage V_{th} ↑, drain current I_d ↓

Interface roughness

Variation of the threshold voltage V_{th} at $V_{ds}=0.4$ V
[110], [100], [111], and [112]

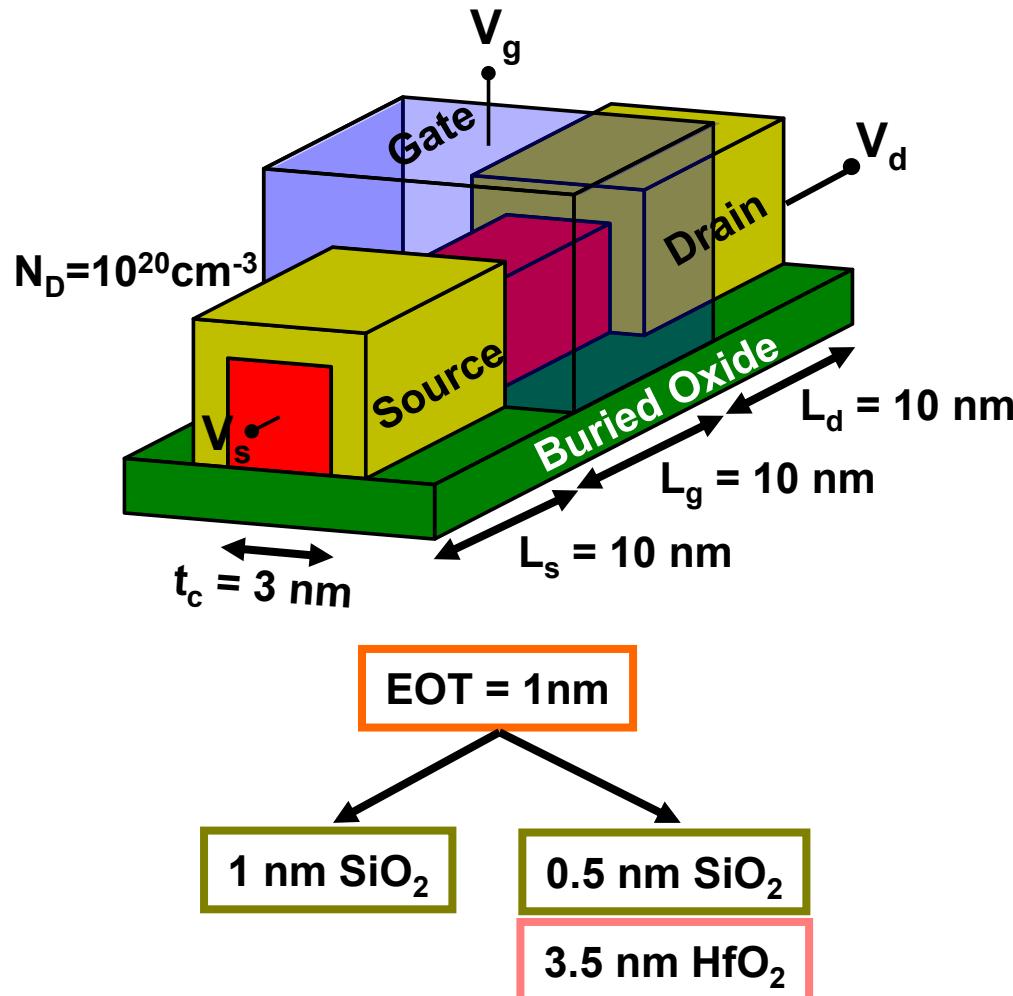


Performance summary: best channel orientation?

	n- I_{on} [μA]	p- I_{on} [μA]	σ [mV]	S @ $L_g=4$ nm
[110]	7.7	15.7	9.3	122.3
[100]	4.3	2.3	13.8	88.5
[112]	2.9	9.2	16.8	87.5
[111]	1.2	10.3	24.8	83.2

[110] has the highest on-current, is the least sensitive to interface roughness, but suffers the most from source-to-drain tunneling

Triple-gate nanowire FET with poly-Si or TiN contacts



Popular 3D mode-space approx. not suited => multi-terminal real space simulator (eff. mass!)

3D Schrödinger equation

$$\mathbf{H} |\Psi_E\rangle = E |\Psi_E\rangle$$

eff. mass approx. + finite difference

$$\langle \mathbf{r} | \Psi_E \rangle = \sum_{ijk} C_{ijk}(E) \delta(\mathbf{r} - \mathbf{R}_{ijk})$$

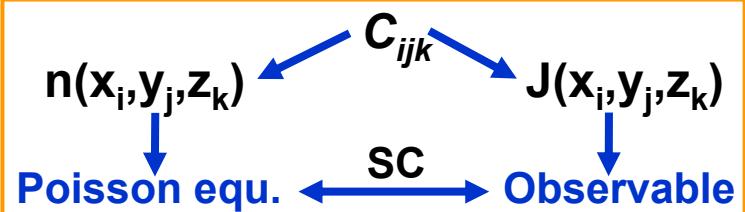
3D sparse linear problem $\mathbf{A}\mathbf{x}=\mathbf{b}$

$$(\mathbf{E} - \mathbf{H} - \Sigma_S - \Sigma_D - \Sigma_G) \cdot \mathbf{C} = \mathbf{S}_{\text{Inj}} + \mathbf{D}_{\text{Inj}} + \mathbf{G}_{\text{Inj}}$$

BCs and injection mechanism

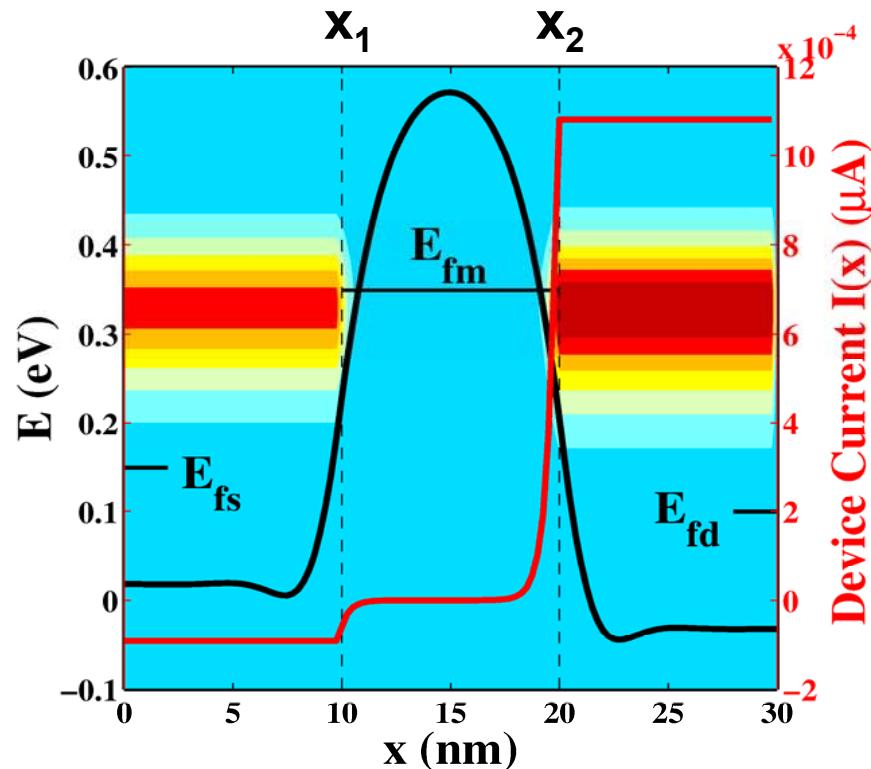
$$\mathbf{M} \cdot \mathbf{C}_B = 2 \cdot \cos(k_B) \cdot \mathbf{C}_B$$

3D carrier and current densities

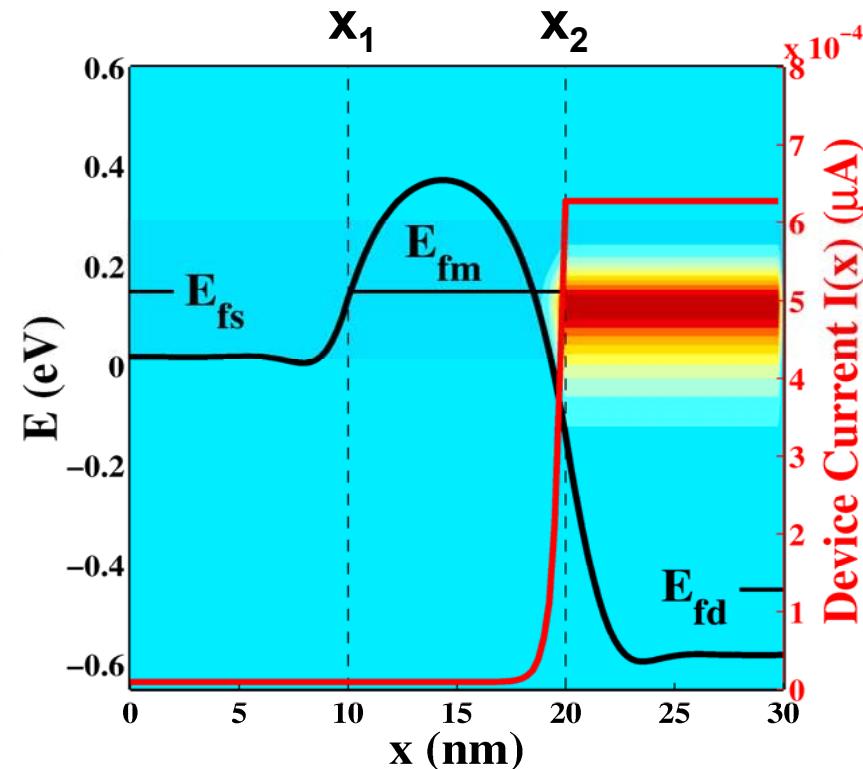


Spectral gate current and total device current along the x -axis

$$V_{gs} = -0.2 \text{ V}, V_{ds} = 0.05 \text{ V}$$



$$V_{gs} = 0.0 \text{ V}, V_{ds} = 0.60 \text{ V}$$

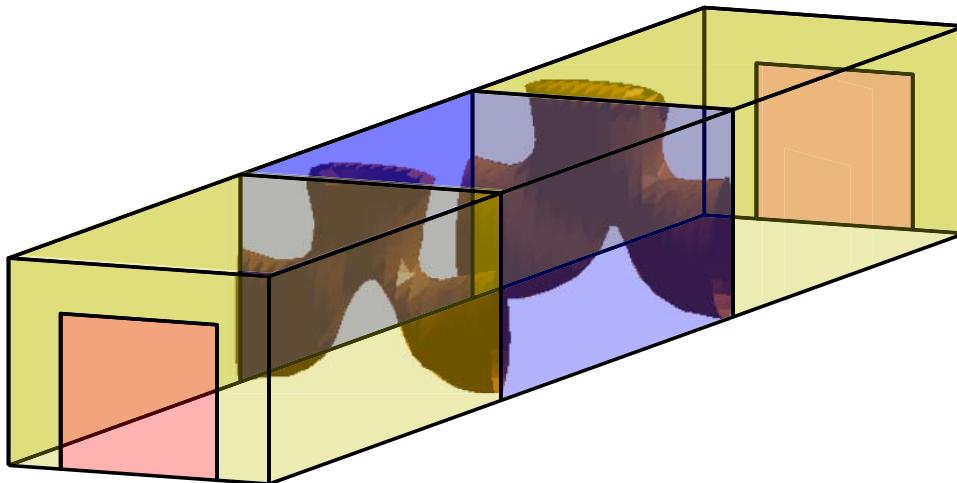


current conservation: $I(x_2) - I(x_1) = I_G$

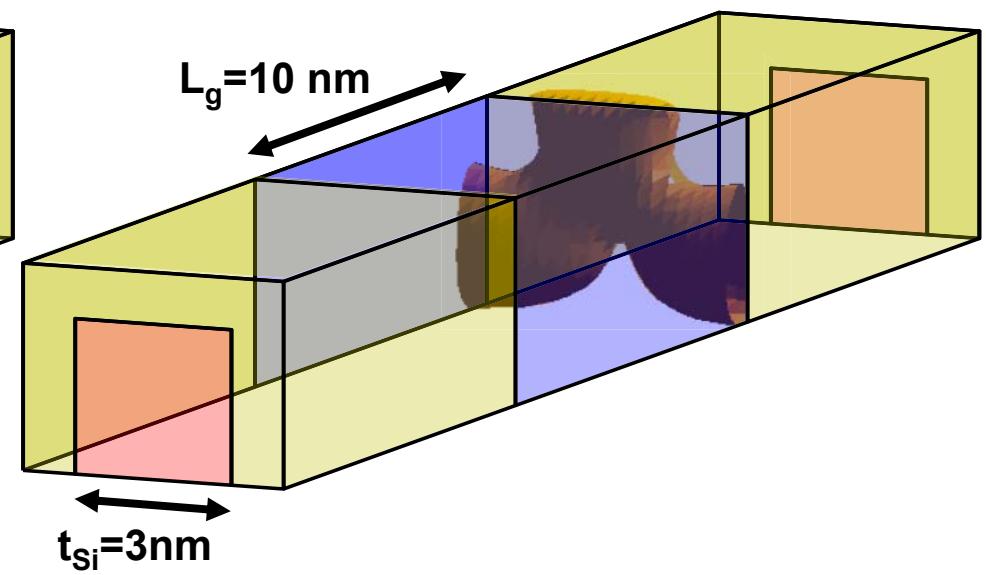
Isosurfaces of the gate current for a triple-gate structure SiO_2 dielectric layer + TiN metal contact

Current escapes at the **gate corners**

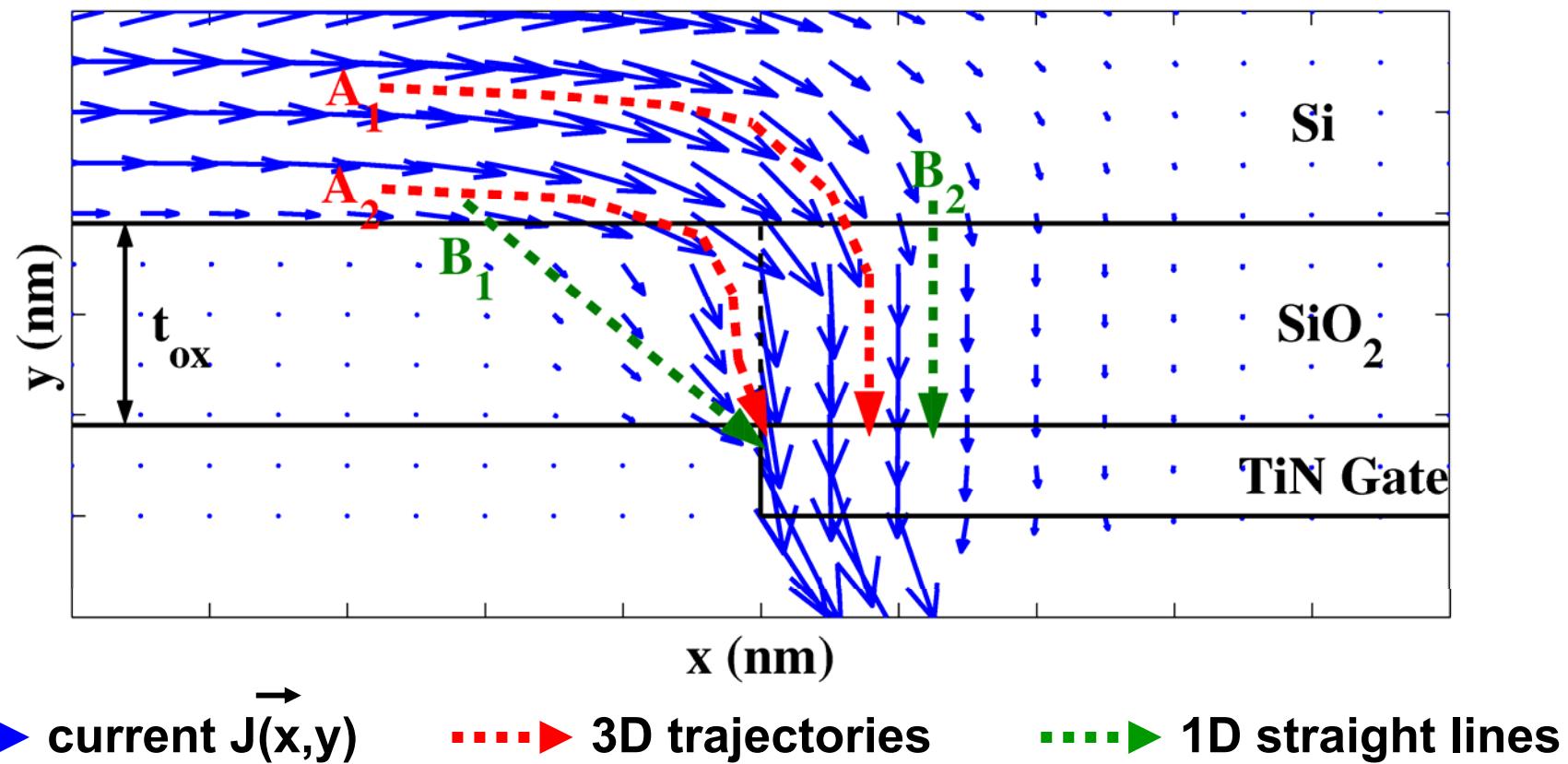
$$V_{gs} = -0.2 \text{ V}, V_{ds} = 0.05 \text{ V}$$



$$V_{gs} = 0.0 \text{ V}, V_{ds} = 0.60 \text{ V}$$



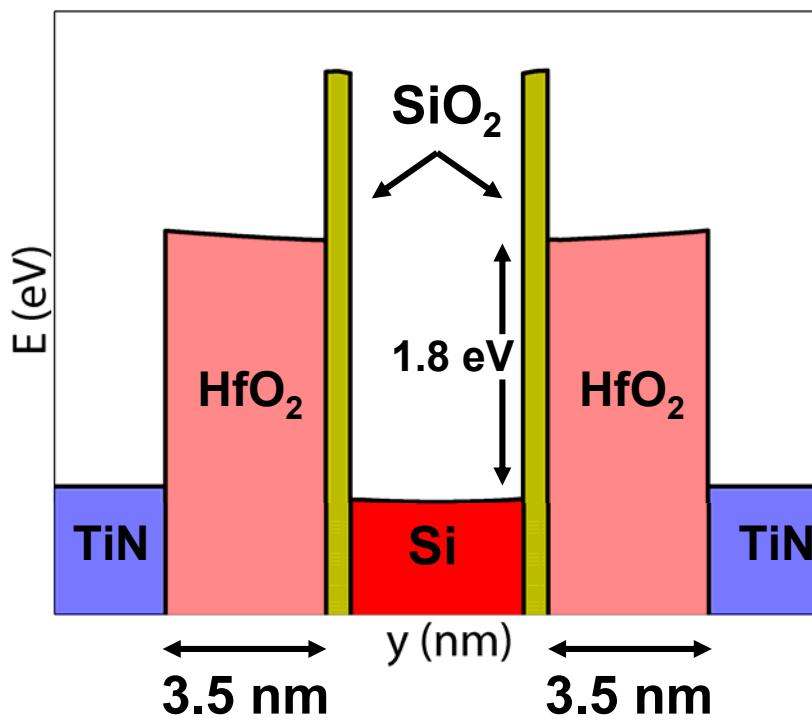
Current trajectories around gate corner
1D approximation vs full 3D (projected)



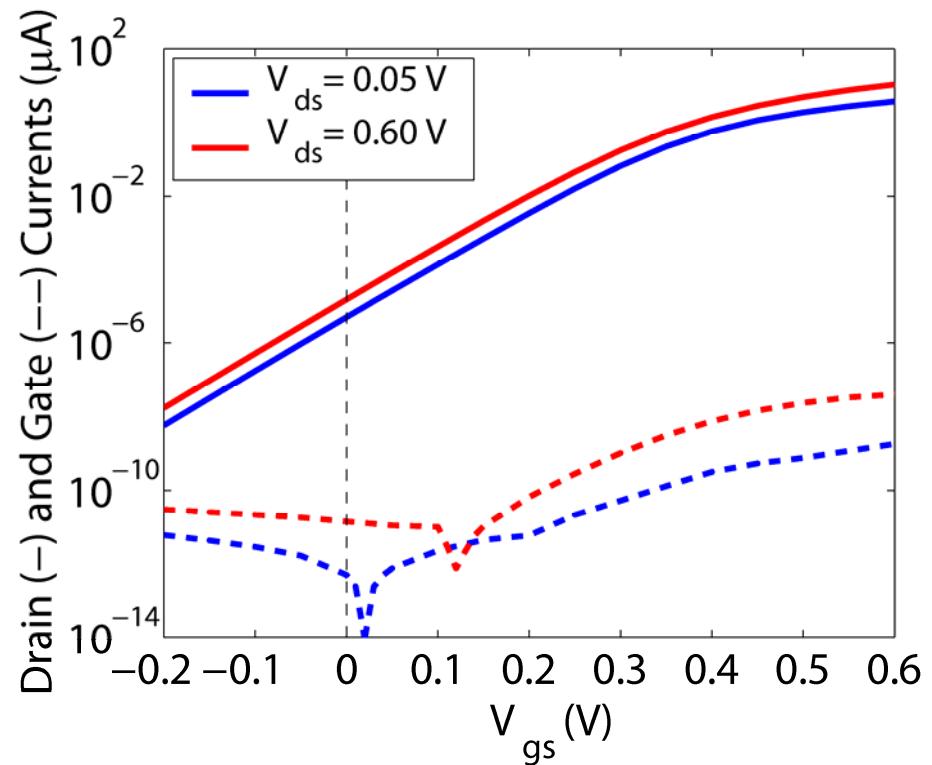
Gate stack: SiO_2 (0.5 nm) + HfO_2 (3.5 nm, $m^* = 0.2 m_0$, $\epsilon_R = 25$)

Performance: Good threshold voltage V_{th} , low off-current I_{OFF}

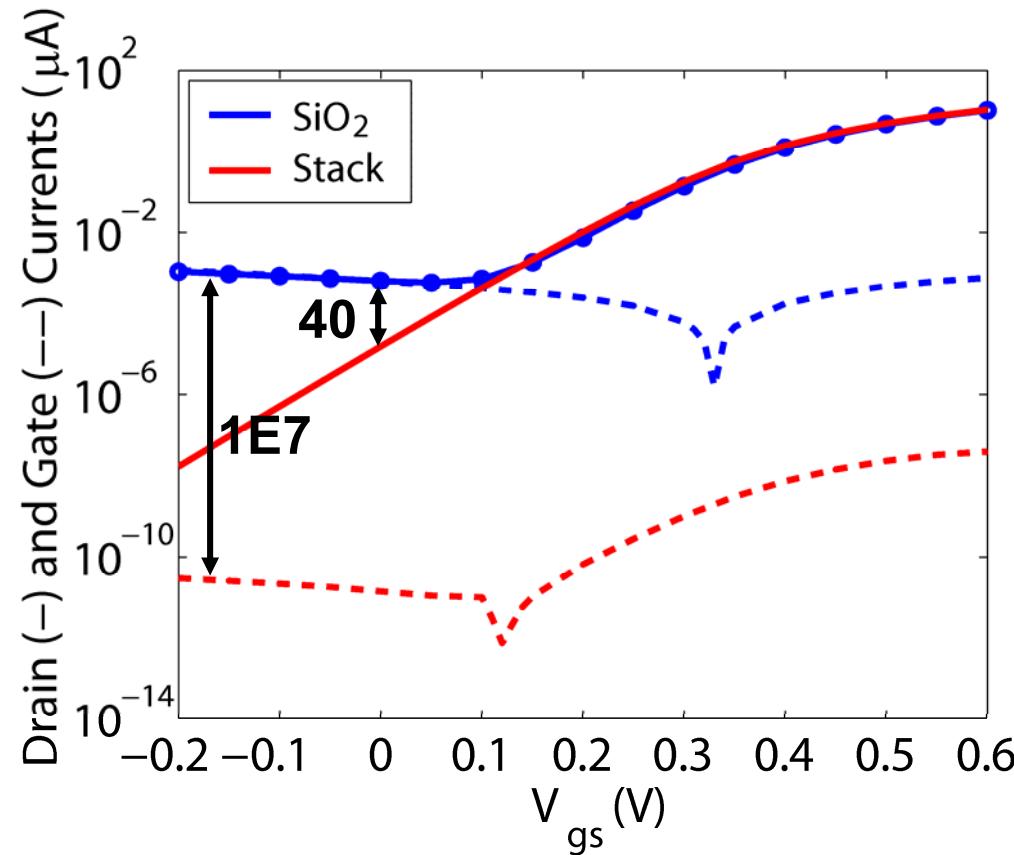
Dielectric + Contact Structure



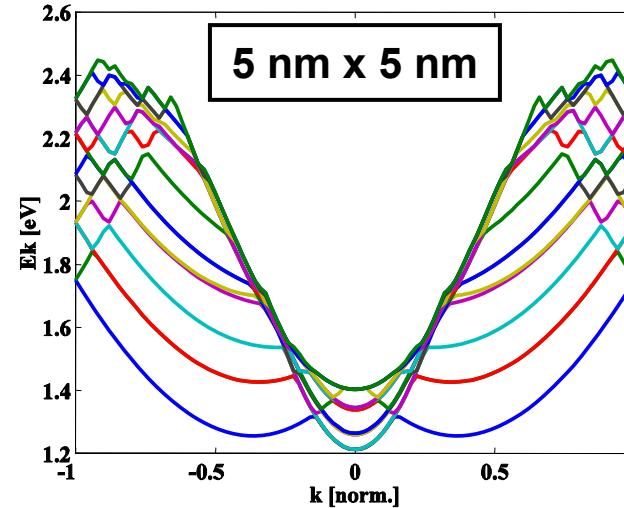
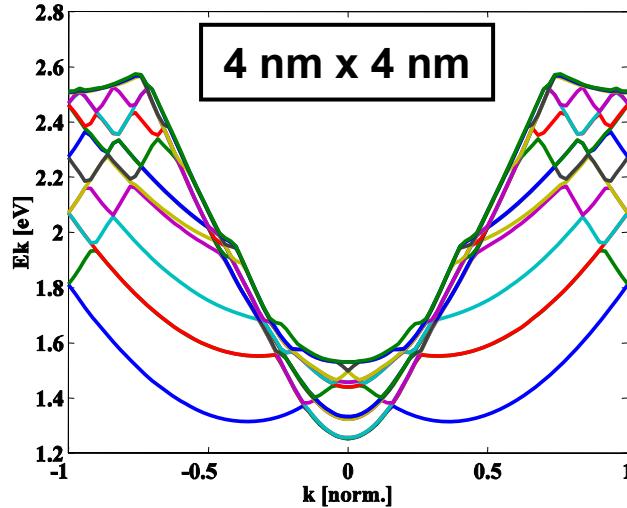
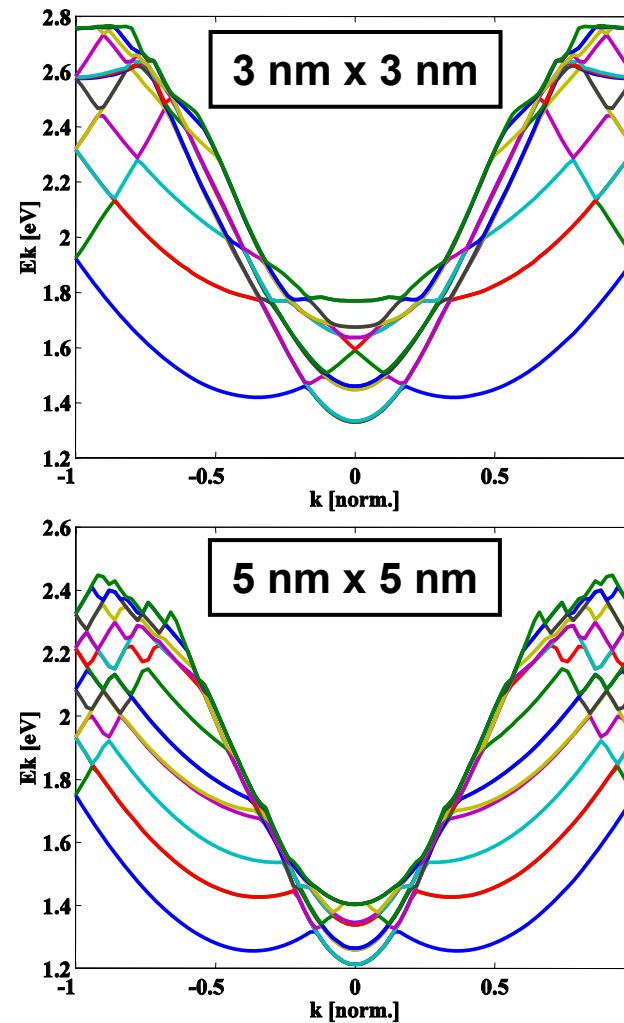
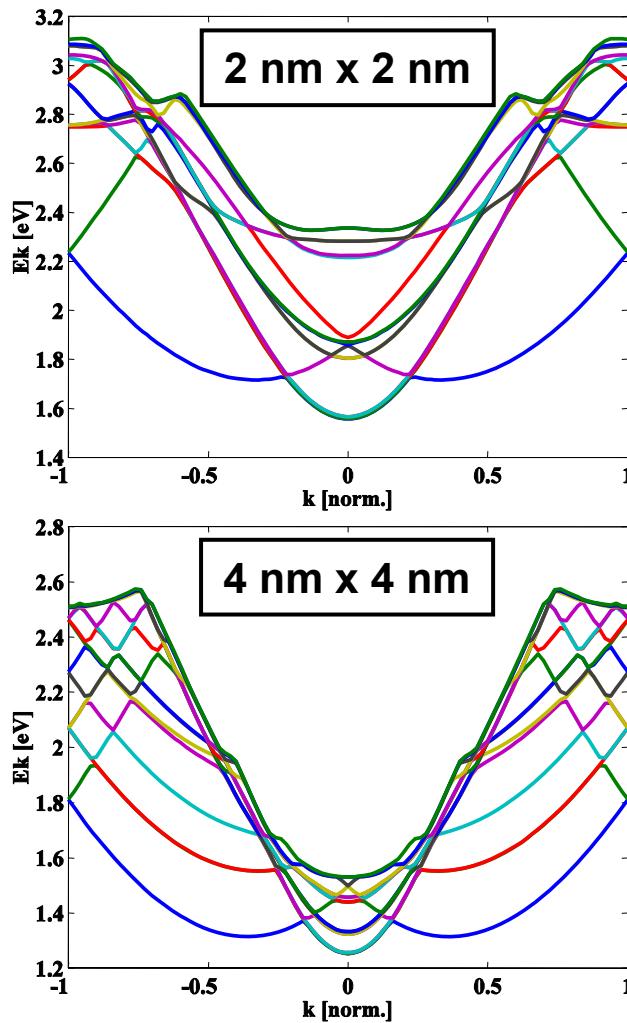
Transfer Characteristics



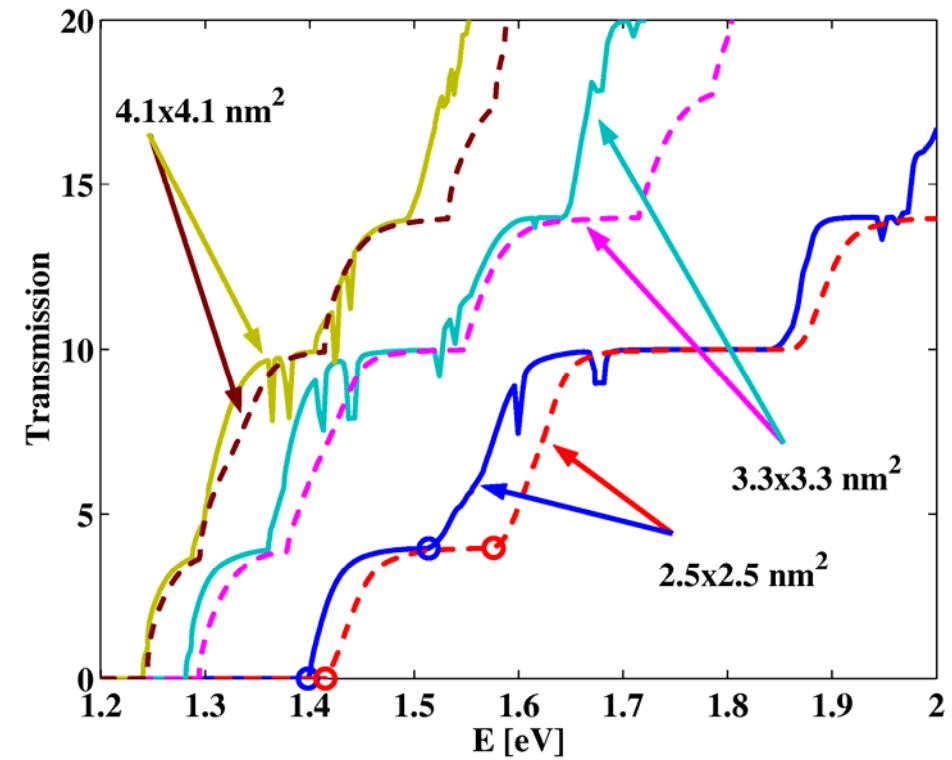
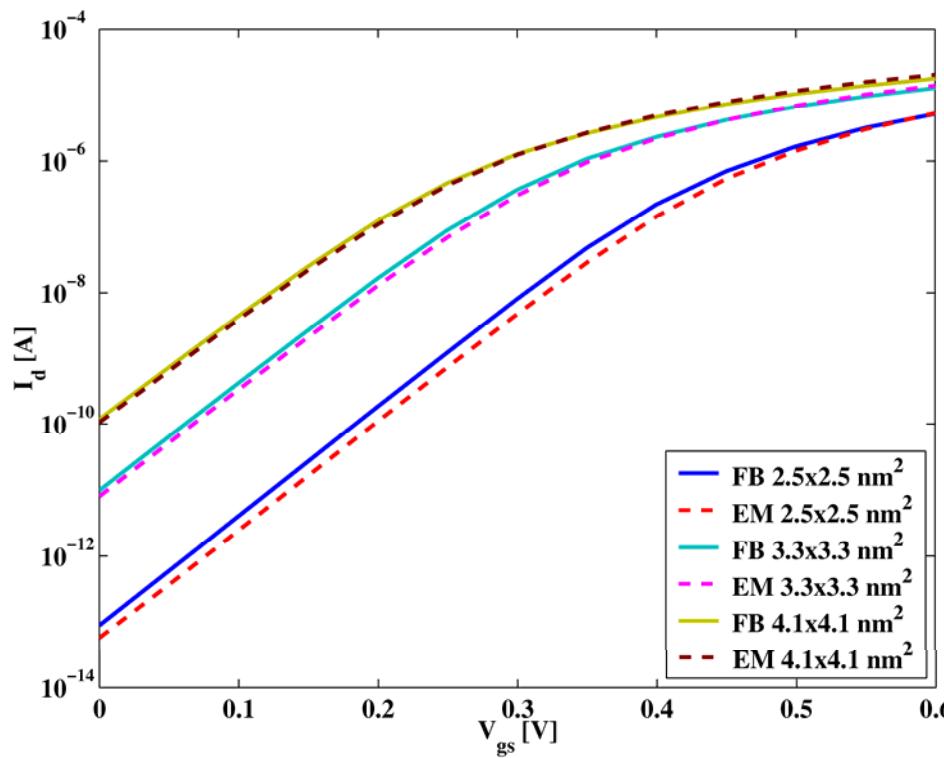
Stack reduces gate current by 7 orders of magnitude at low V_{gs} , off-current by a factor of 40, and keeps the same on-current



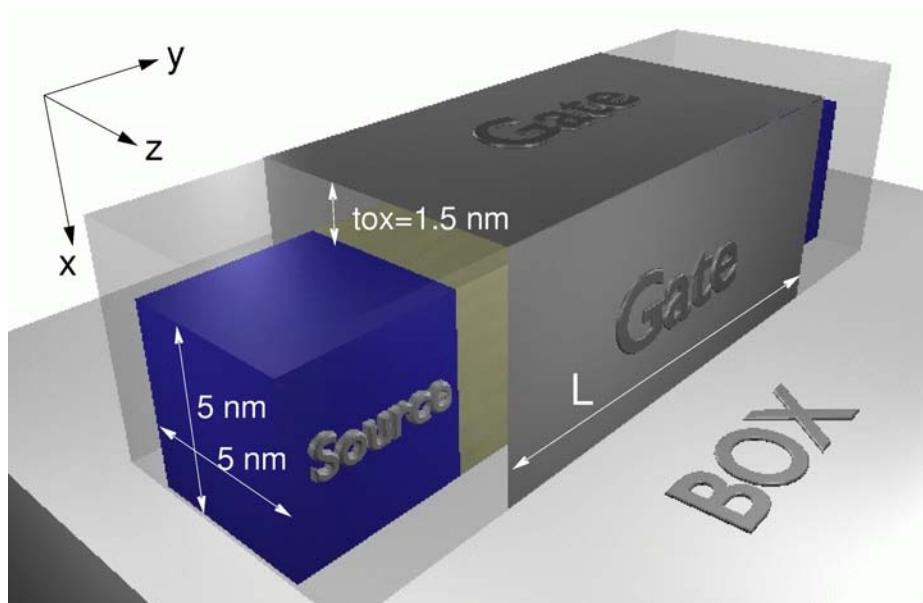
How does band structure change with increasing cross section?



Output characteristics and transmission for [100]
Full Band (solid lines) vs Effective Mass (dashed lines)



Example: Triple-gate 5nm x 5nm NW FET



TGNW-FET (courtesy EU SINANO project)

Gate length: 25 nm (65 nm technology node).

Channel Cross-Section : Square ($5 \times 5 \text{ nm}^2$).

Source/drain extensions: 10 nm.

Oxide parameters: material is SiO_2 ($k \sim 3.9$).

Field Oxide Thickness : 1.5 nm.

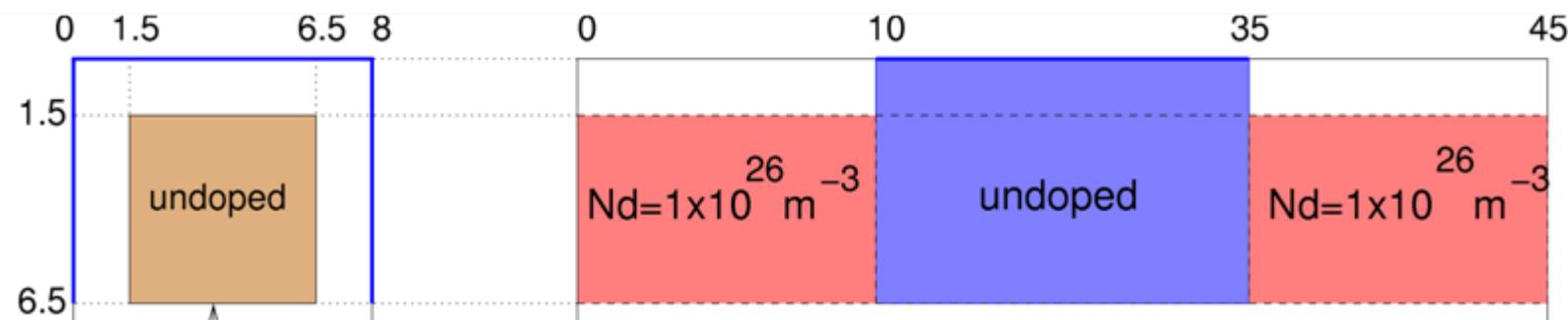
Buried Oxide Thickness : 150 nm.

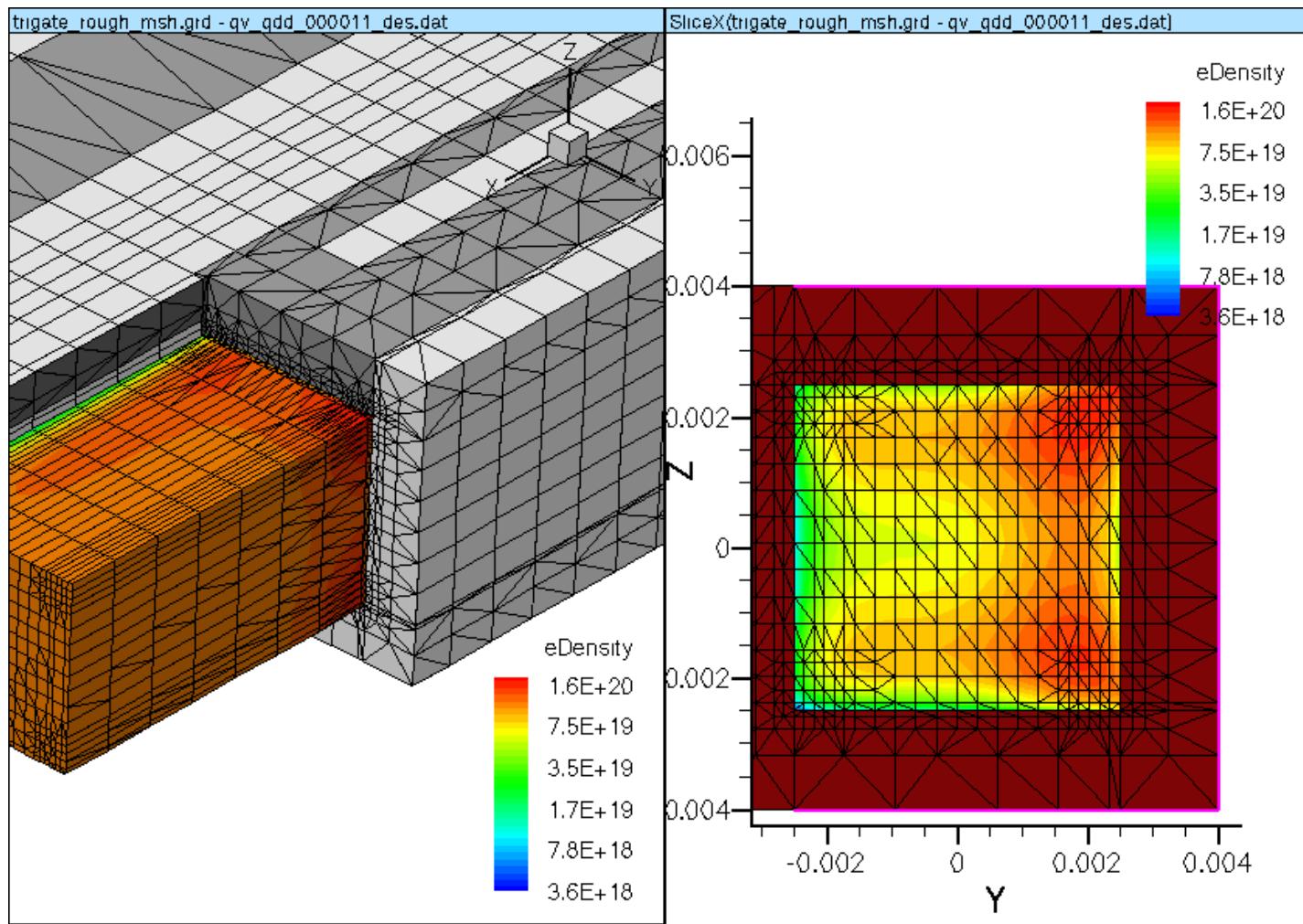
Gate electrode work function: 4.1 eV.

von Neumann boundary conditions at S/D ends.

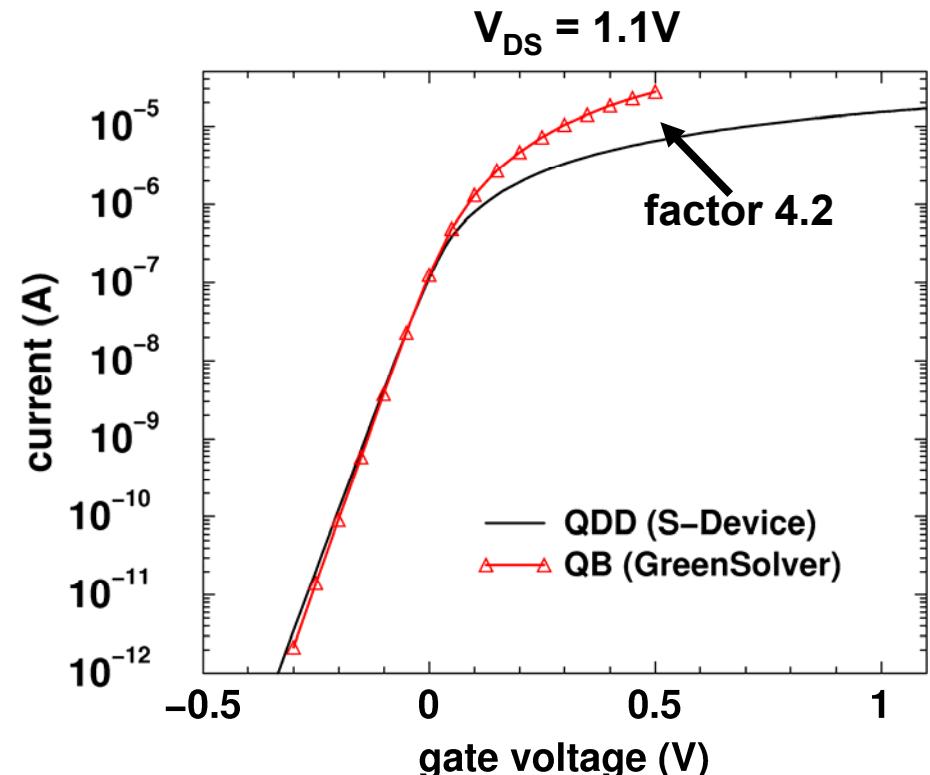
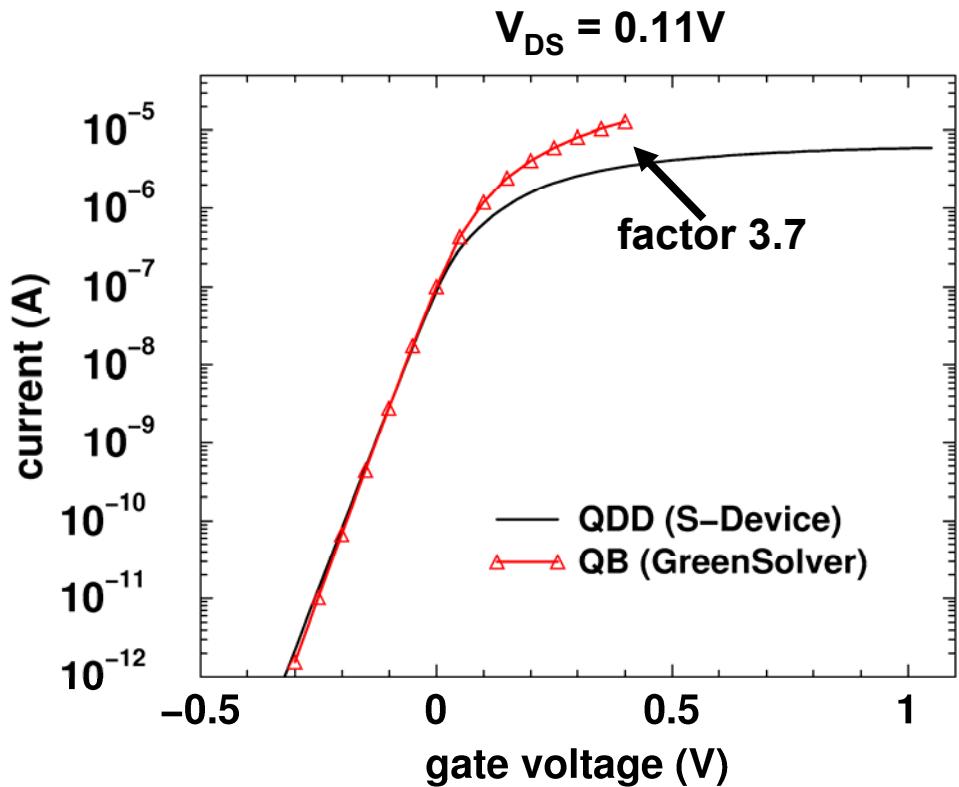
Doping specifications:

Substrate undoped, source and drain: $N_d = 10^{20} \text{ cm}^{-3}$.



S-DEVICE mesh for the TGNW-FET and electron density at $V_{GS} = 1.1V$ 

Comparison of currents



Example: acoustic phonon scattering

Assumptions: Bulk phonon dispersion, bulk coupling constants, EMA

Simplifications: High-T appr. $\hbar\omega_q \ll k_B T$, lin. dispersion $\hbar\omega_q = c_s q$

→ self-energy $\Sigma_{ac}^<$ becomes local in space

$$\Sigma_{ac}^<(r, r', E) = \frac{\Xi^2 k_B T}{\rho c_s^2} G^<(r, r', E) \delta(r - r')$$

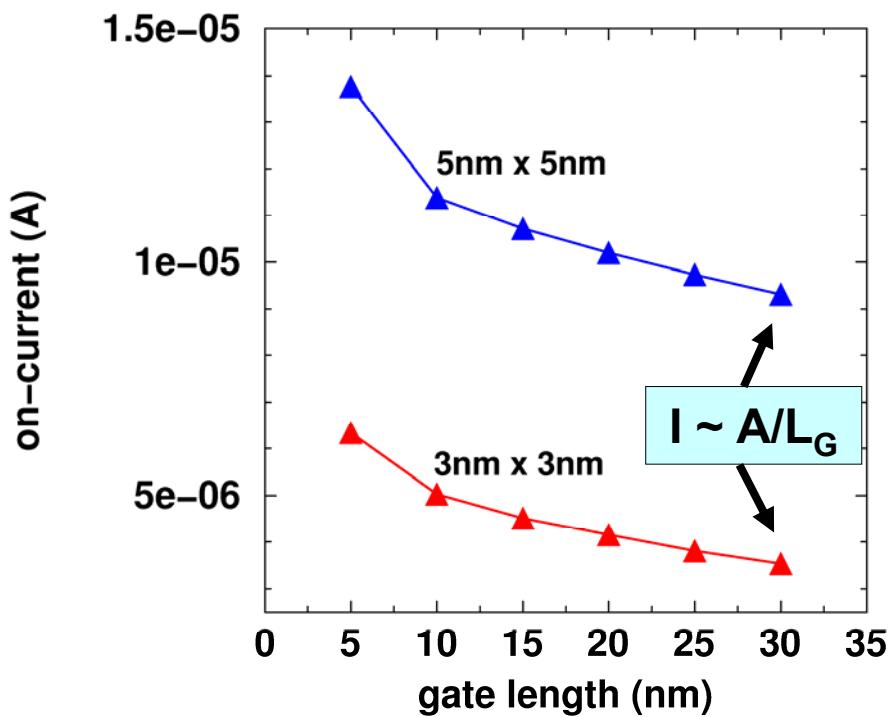
$G^<$ requires G^R (full size): $G^< = G^R (\Sigma_S^< + \Sigma_D^< + \Sigma_{ac}^<) G^A$

$$(E - H - \Sigma^R) G^R = 1$$

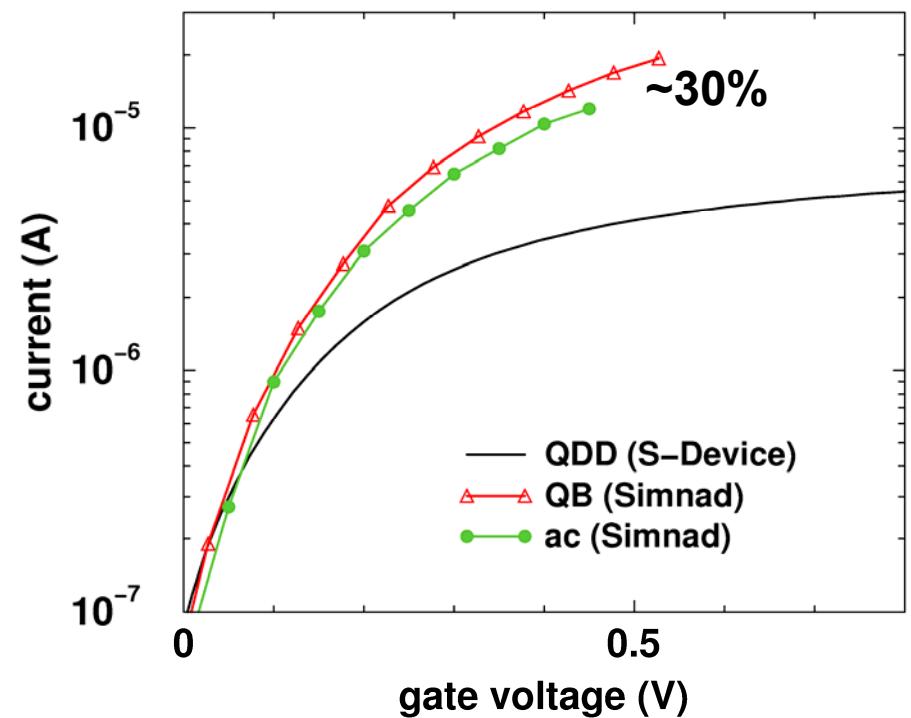
Elastic appr. $E \pm \hbar\omega_q \approx E$ → Σ^R simplifies to

$$\Sigma^R(r, r', E) = \frac{\Xi^2 k_B T}{\rho c_s^2} G^R(r, r', E) \delta(r - r')$$

Scaling @ $V_{GS}=0.5V$, $V_{DS}=50mV$



TG $5 \times 5 \times 25 \text{ nm}^3$ NW FET



- Atomistic, full-band approach to simulate Si NW FETs is possible (and justified) up to $5 \times 5 \text{ nm}^2$ cross sections
- A variety of effects (channel orientation, strain, surface roughness, S-D tunneling, gate tunneling) can be studied in the quantum-ballistic limit
- For *ballistic* transport use WF formalism and not NEGF, because much more efficient!
- Challenges: Incoherent scattering, CPU time